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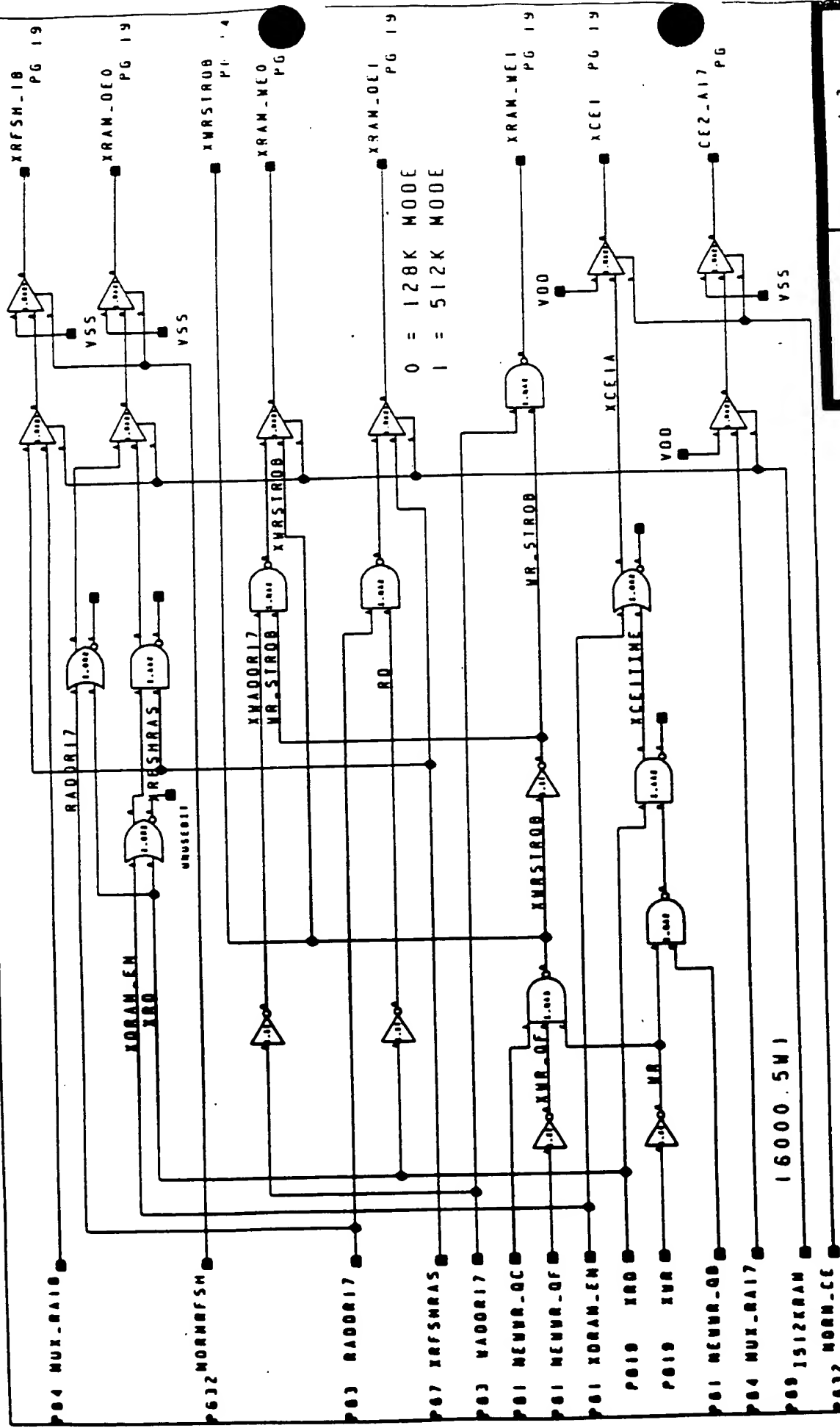
SYSTEM AND METHOD FOR TRANSMITTING AND UTILIZING  
ELECTRONIC PROGRAMS GUIDE INFORMATION

APPENDIX A

**Express Mail No.** EL387001138US



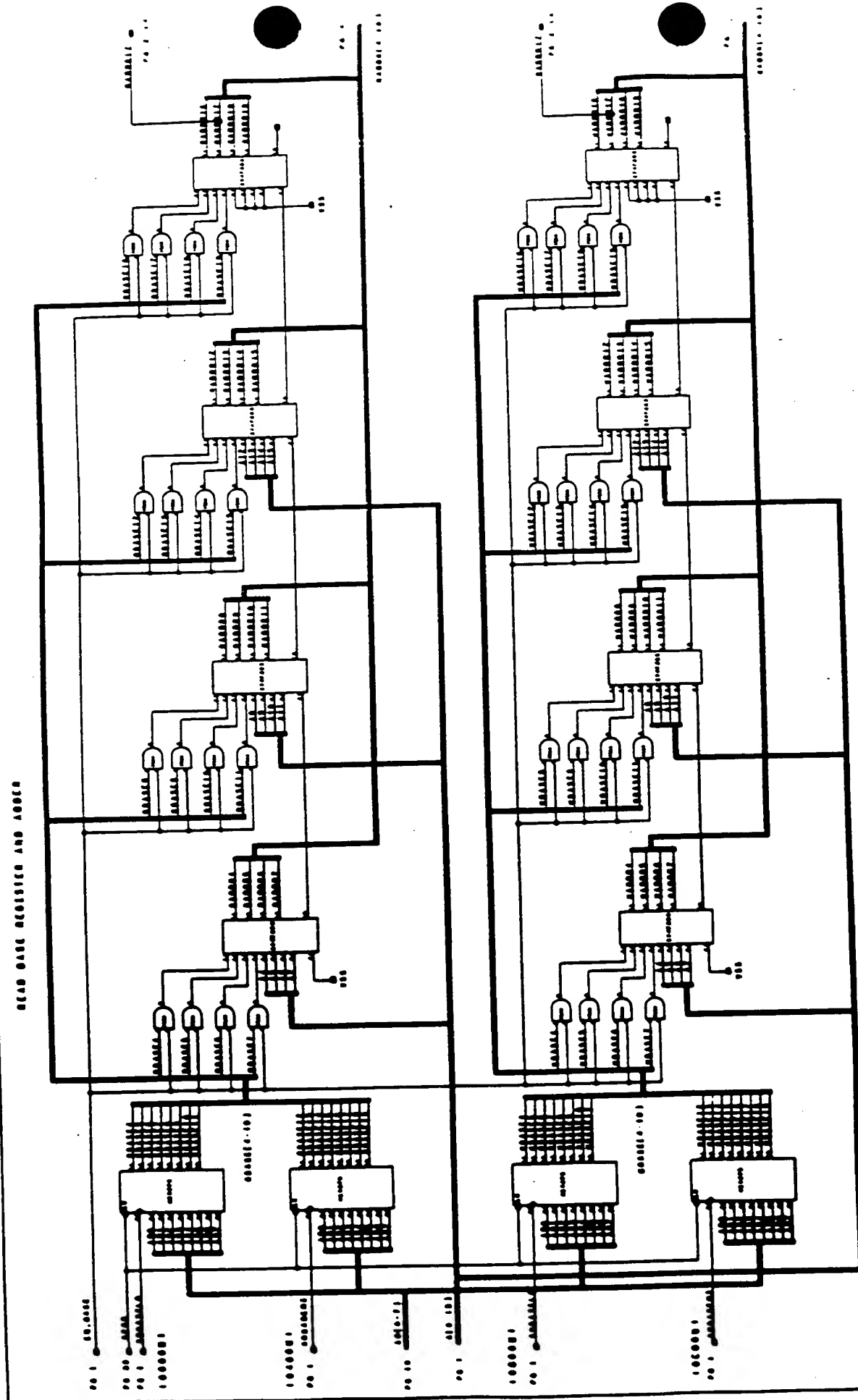
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PSRAM CONTROL LOGIC

SCHEMATIC OF DBE 1200  
 P/N 10-01-00003





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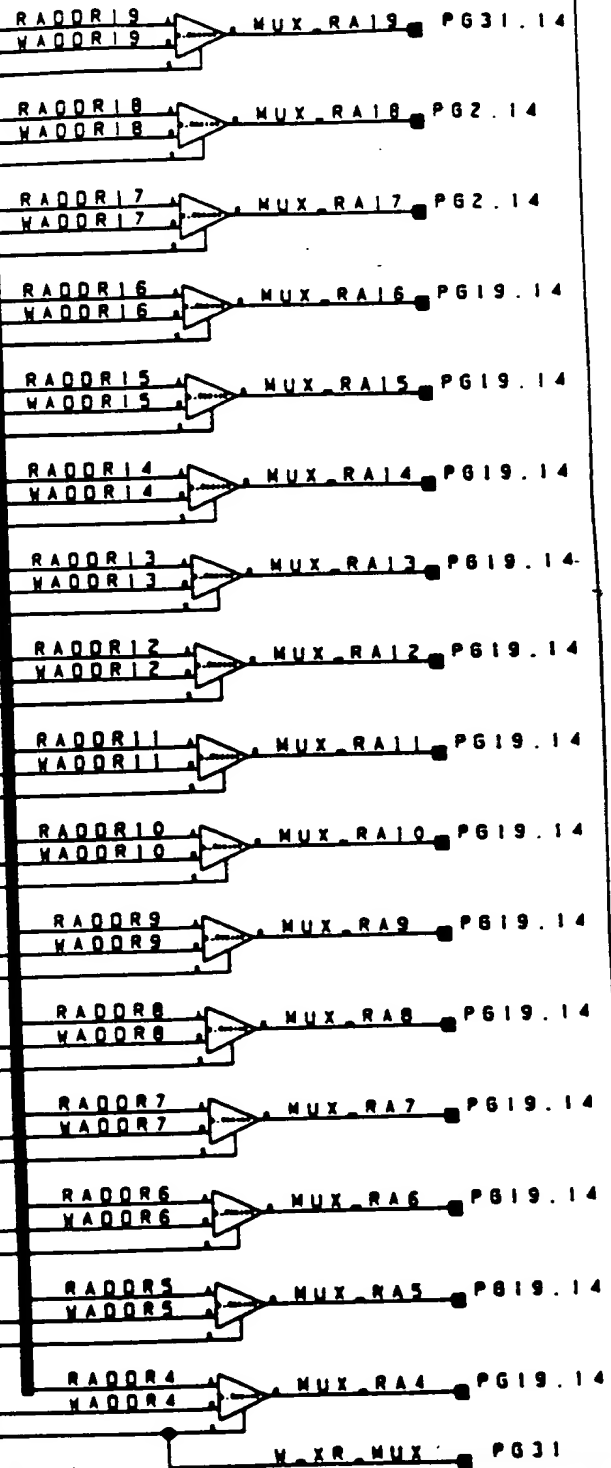
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VDD

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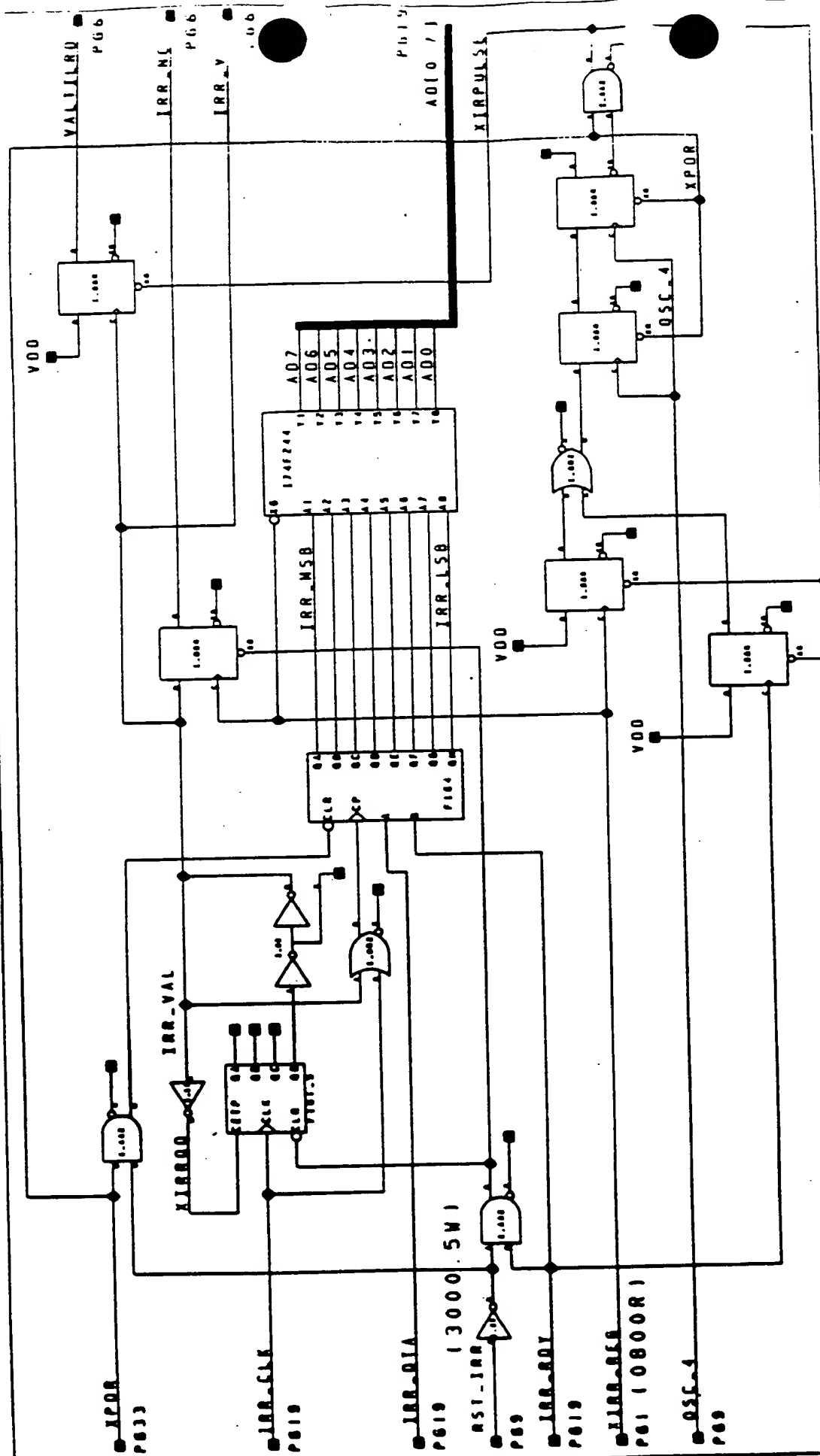
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PG7 XALE

PSRAM ADDRESS MUX



SCHEMATIC DBE 1200  
P/N 10-01-00003

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PAGE 4 OF 80  
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COMMENTS IMP08110

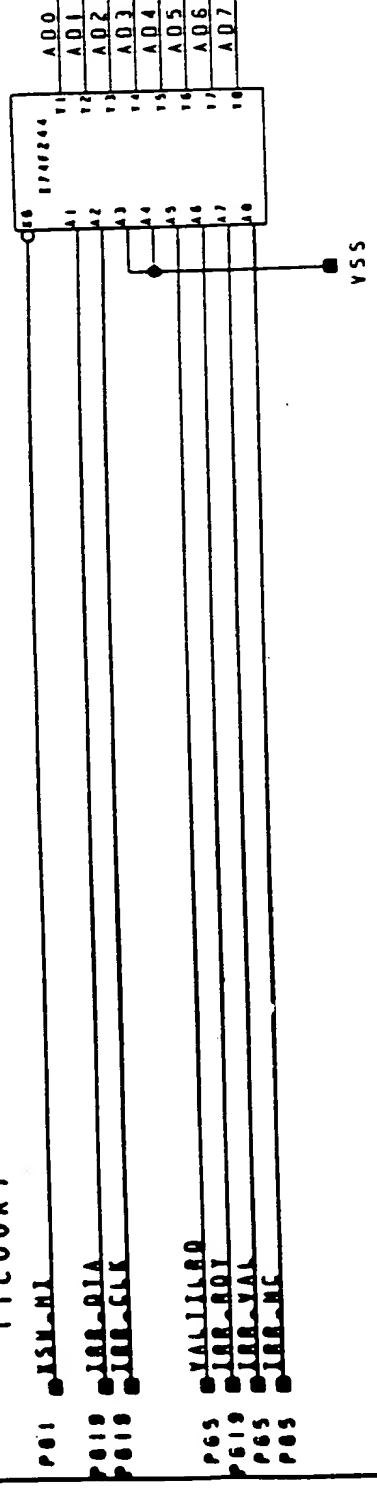


SCHEMATIC. DBE 1200  
P/N 10-01-00003

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REVISION 1  
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PG19

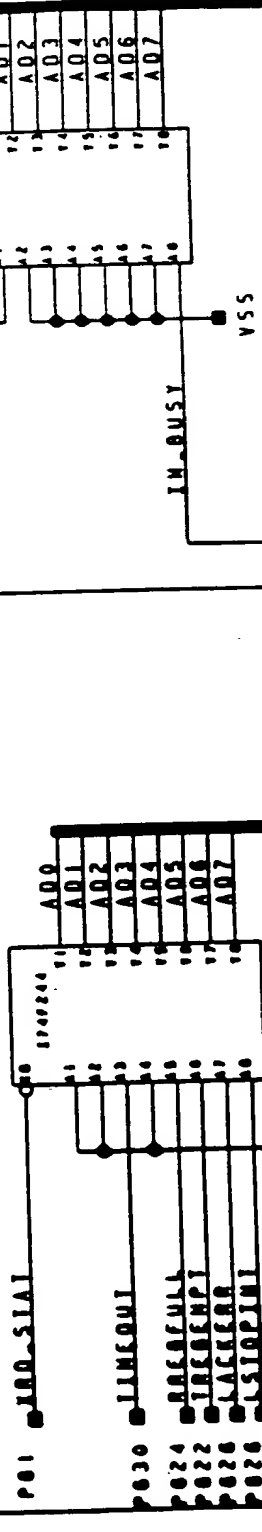


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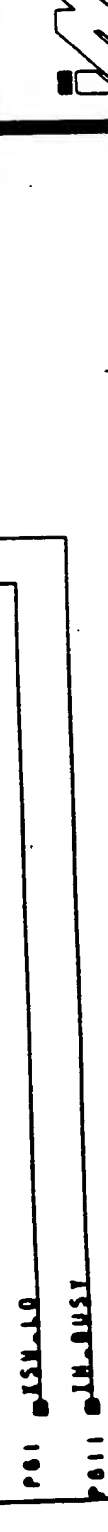
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(1000R)



(11800R)



TITLE A.6

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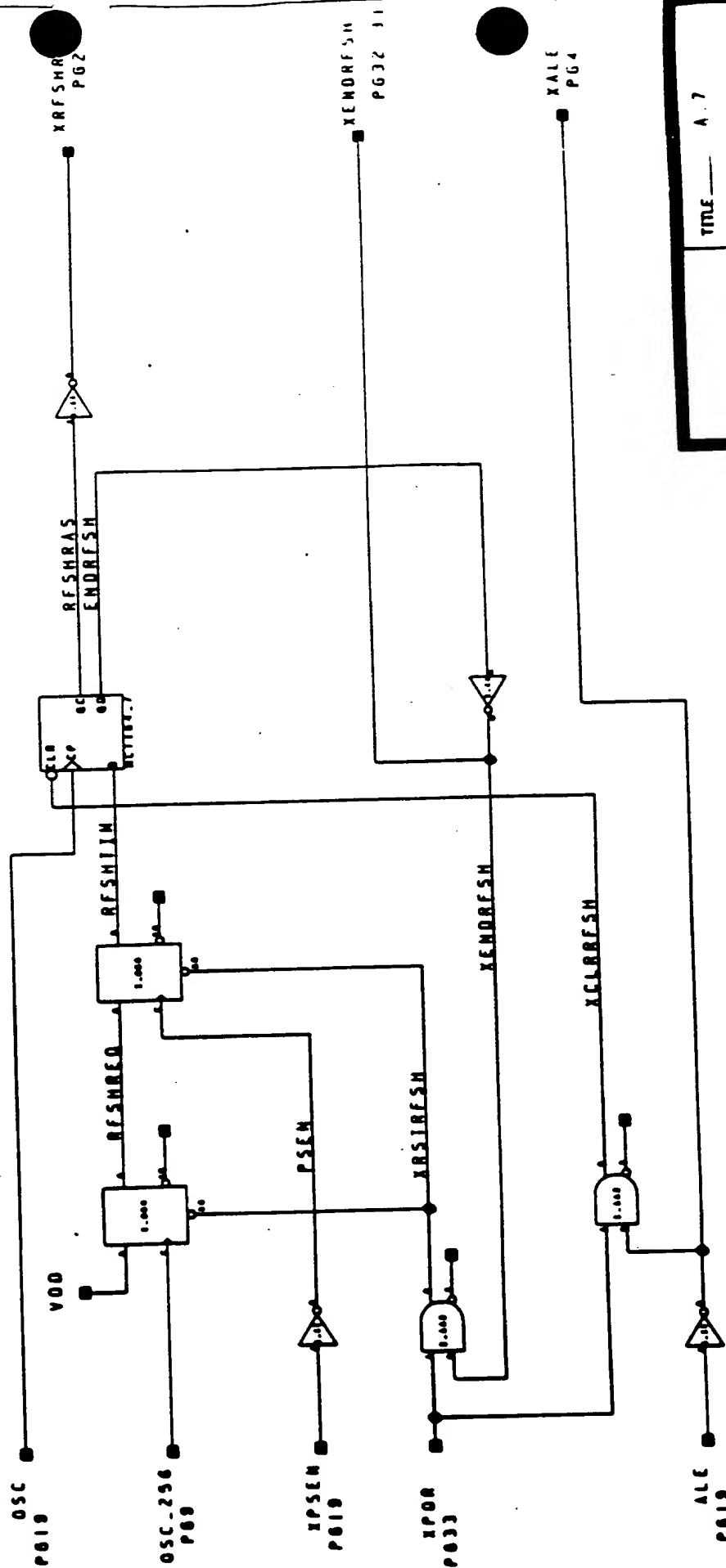
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SCHEMATIC DBE 1200

P/N 10-01-00003

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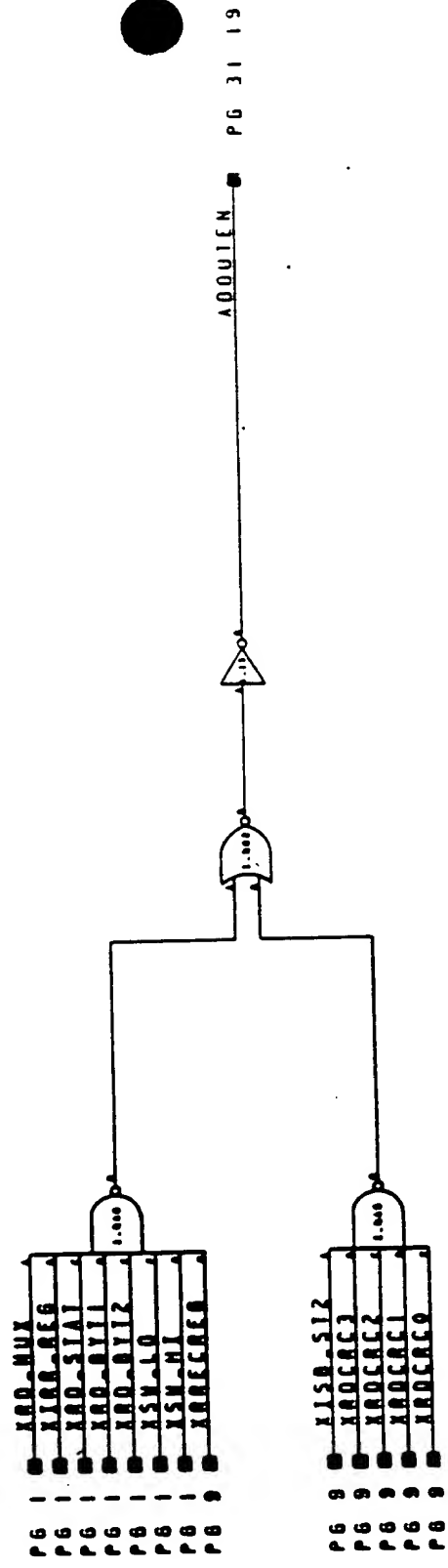
PAGE 7 OF 60

INSIGHT TELECAST  
OF VISION

6190 PMY SUB-600

SCHEMATIC. DBE 1200  
P/N 10-01-00003



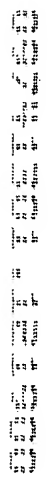


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PAGE 8 OF 60  
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REVISION 1  
COMMENTS WFOG 1.4

SCHEMATIC DBE 1200  
P/N 10-01-00003







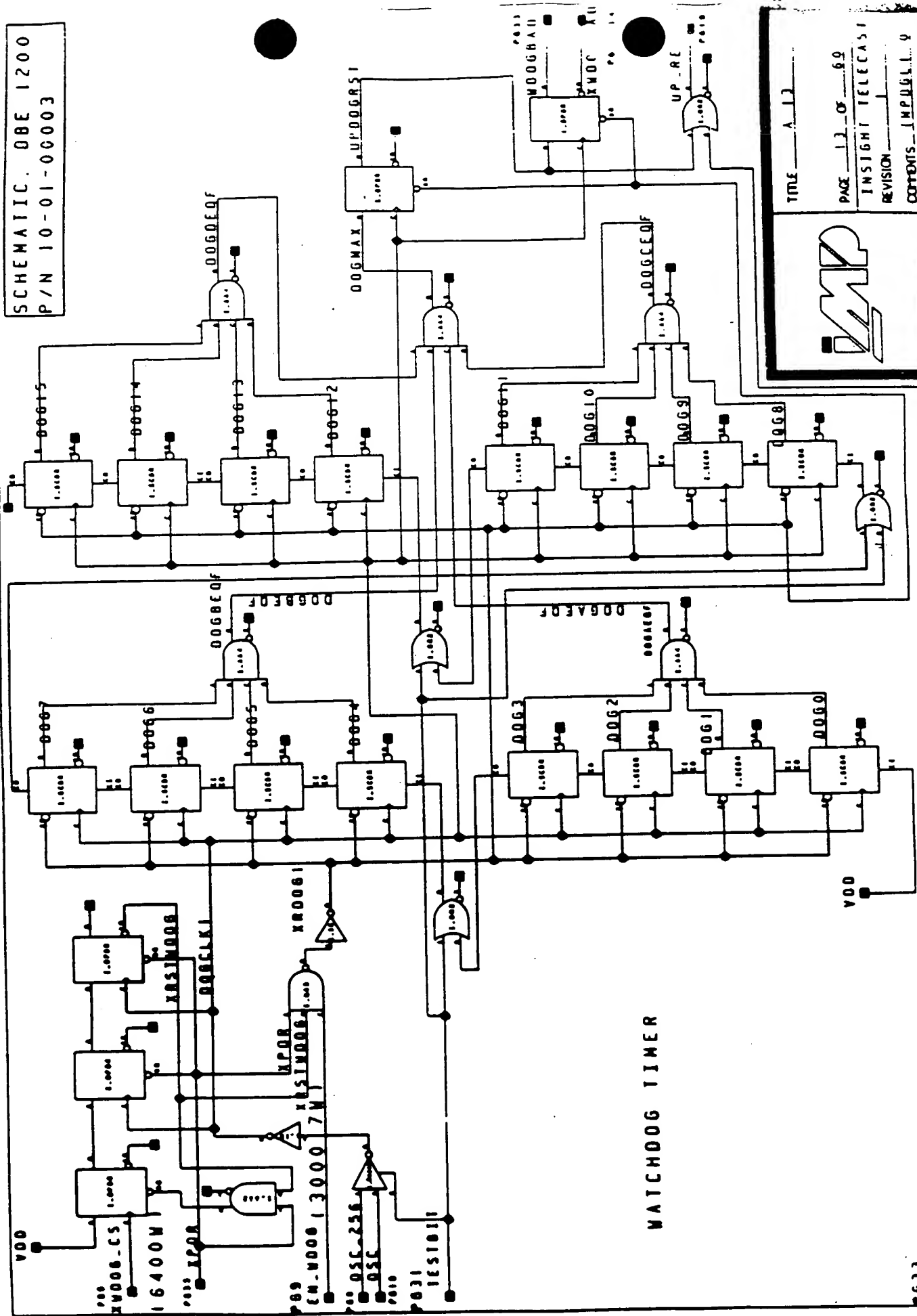



TITLE A 12  
PAGE 12 OF 60  
INSIGHT TELECAST  
REVISION 1  
COMMENTS IMP DGL 1.9

SCHEMATIC. DBE 1200  
P/N 10-01-00003

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SCHEMATIC DBE 1200  
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TIME A 12

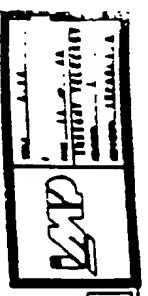
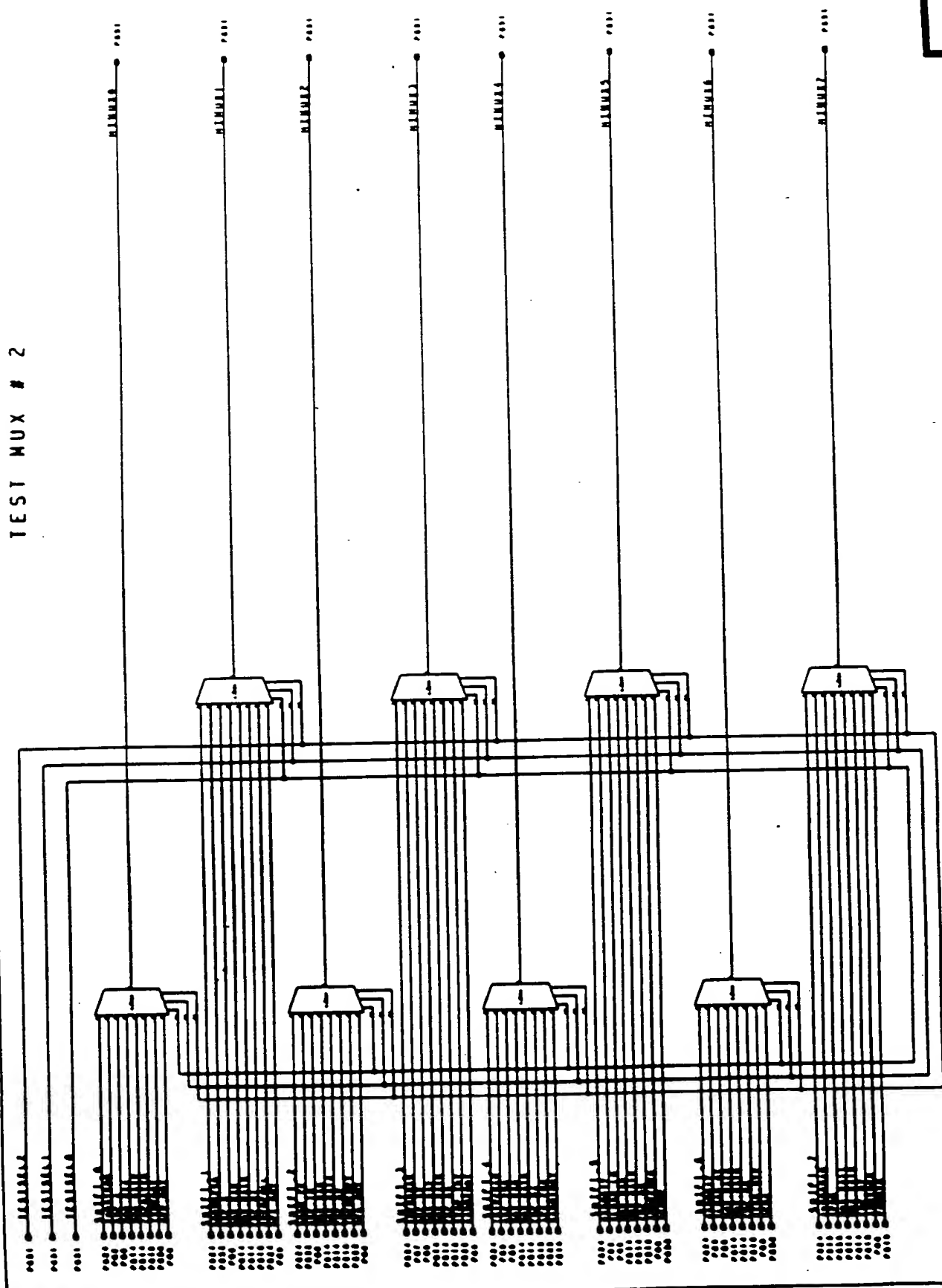
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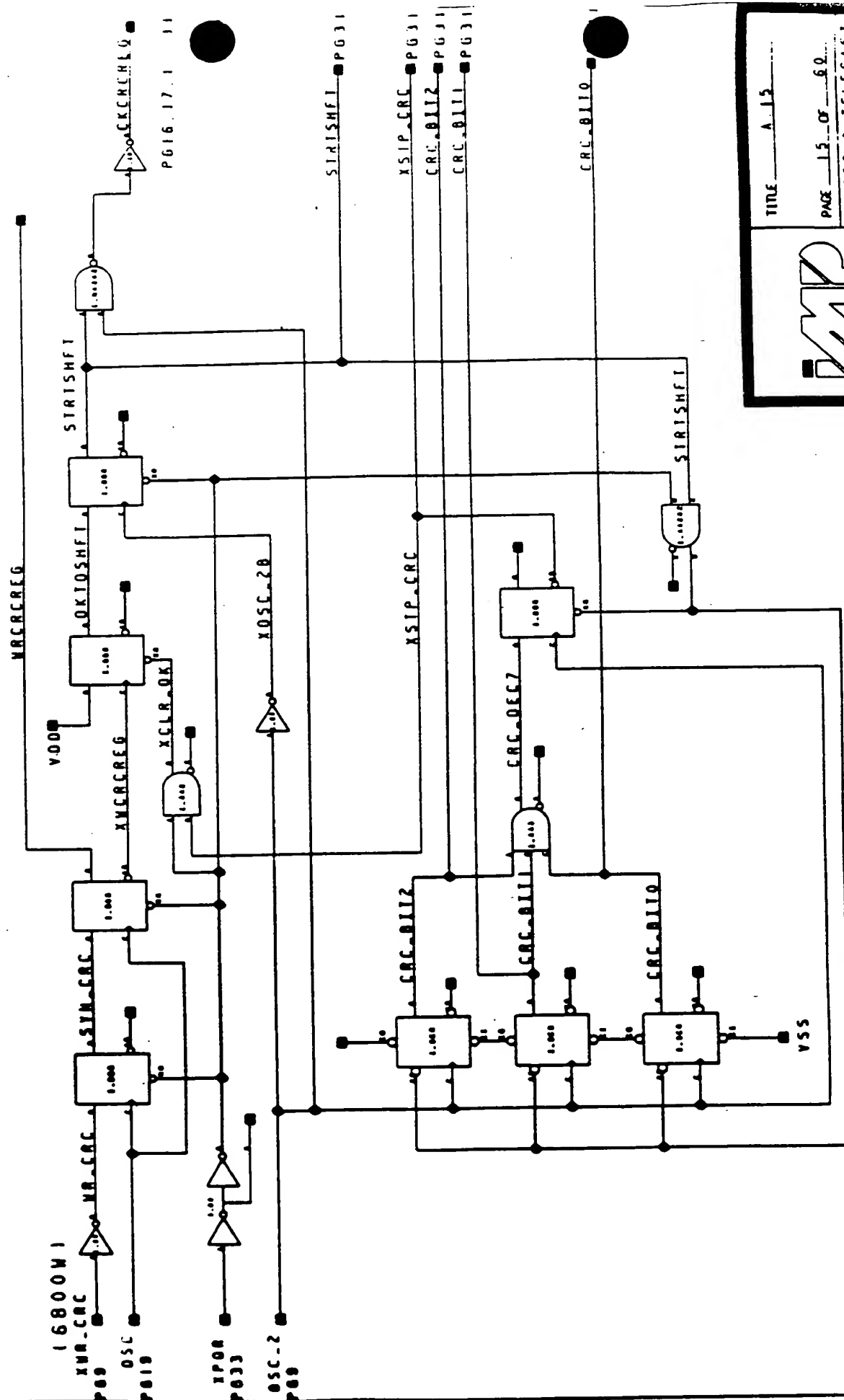
REVISION

CONTENTS IMPUGN 9

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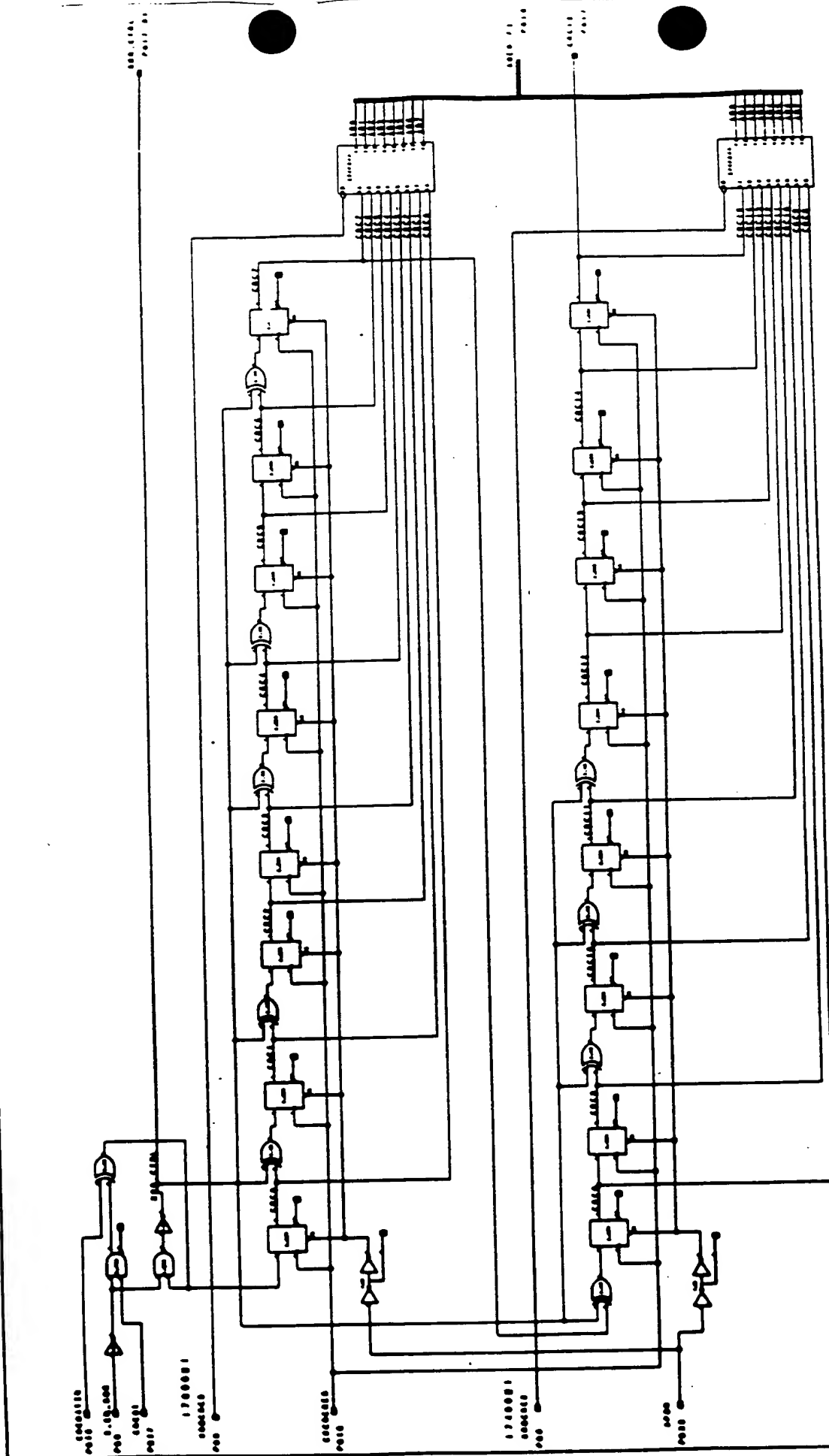
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SCHEMATIC DBE 1200  
P/N 10-01-00003

CRC 32 CONTROL





CRC 32 SHIFT REGISTERS BANK I

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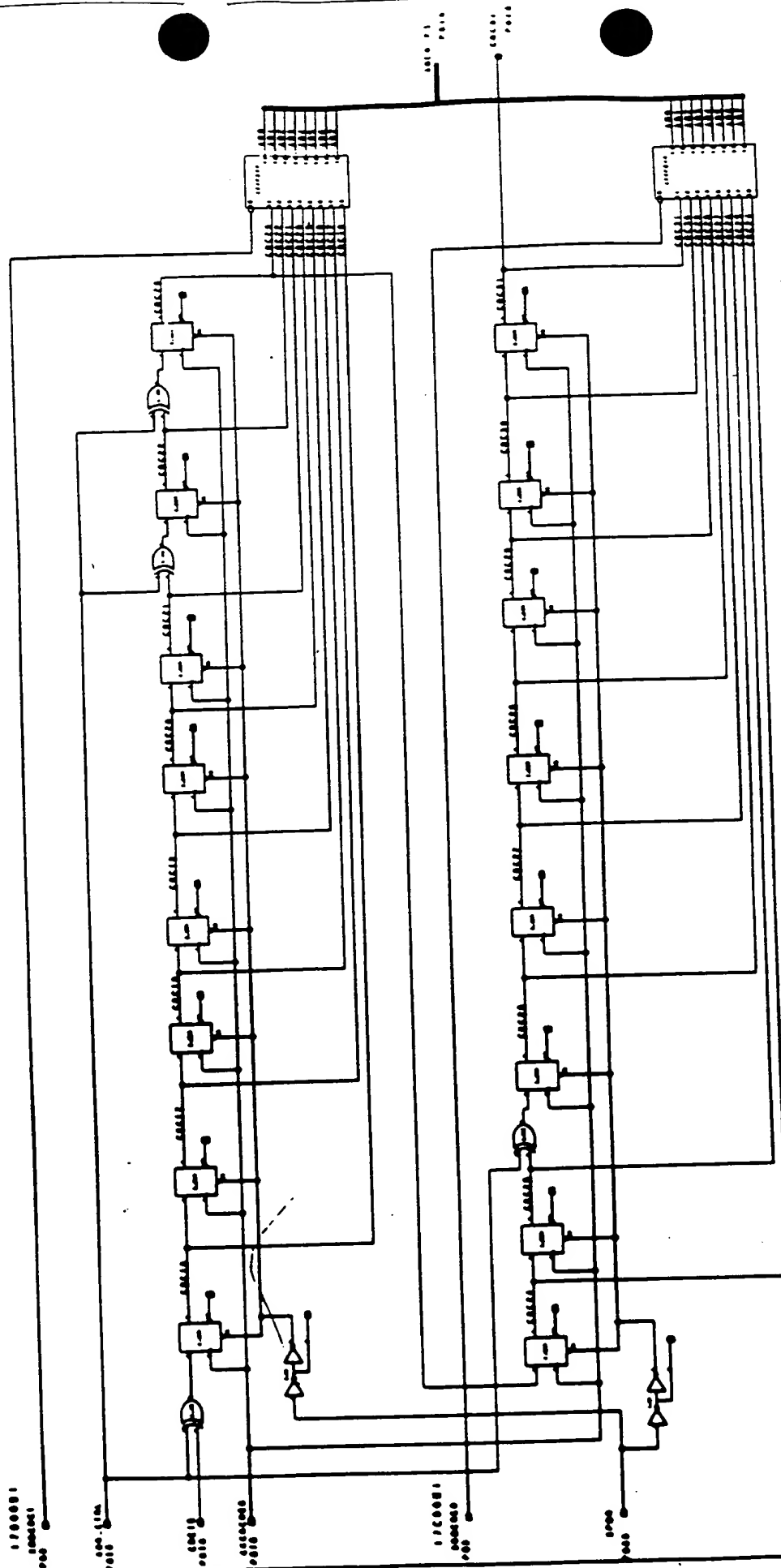
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“11”

**THE UNIVERSITY OF CHICAGO**

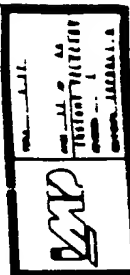
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CRC 32 SHIFT REGISTERS

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(6800W)

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PG9

XPOR  
PG33

CKCRCREG  
PG15

SR-CRC32

AD7

CRC-07

CRC-07 PG14

AD6

CRC-06

CRC-06 PG14

AD5

CRC-05

CRC-05 PG14

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PG19

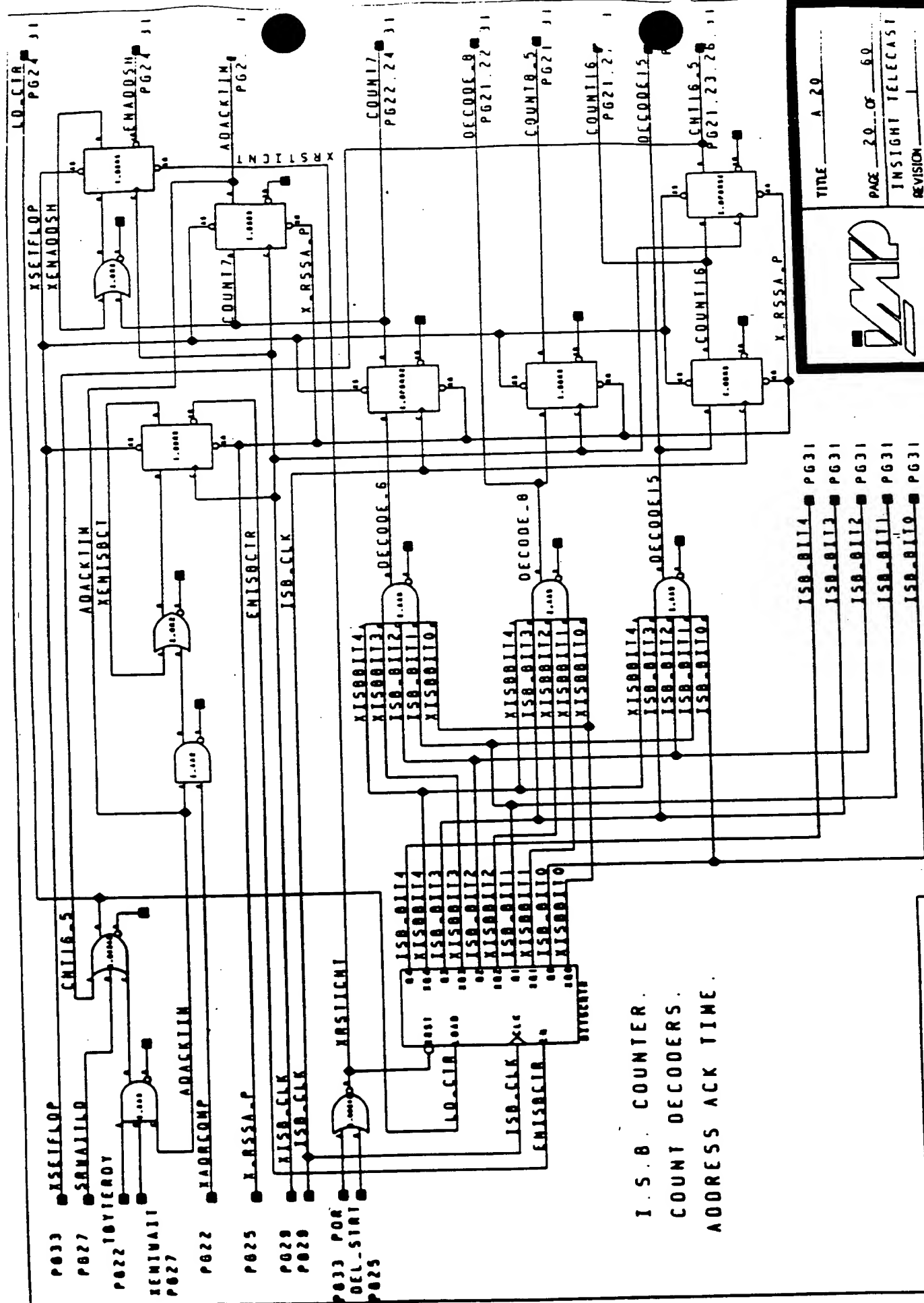
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PAGE 18 OF 60  
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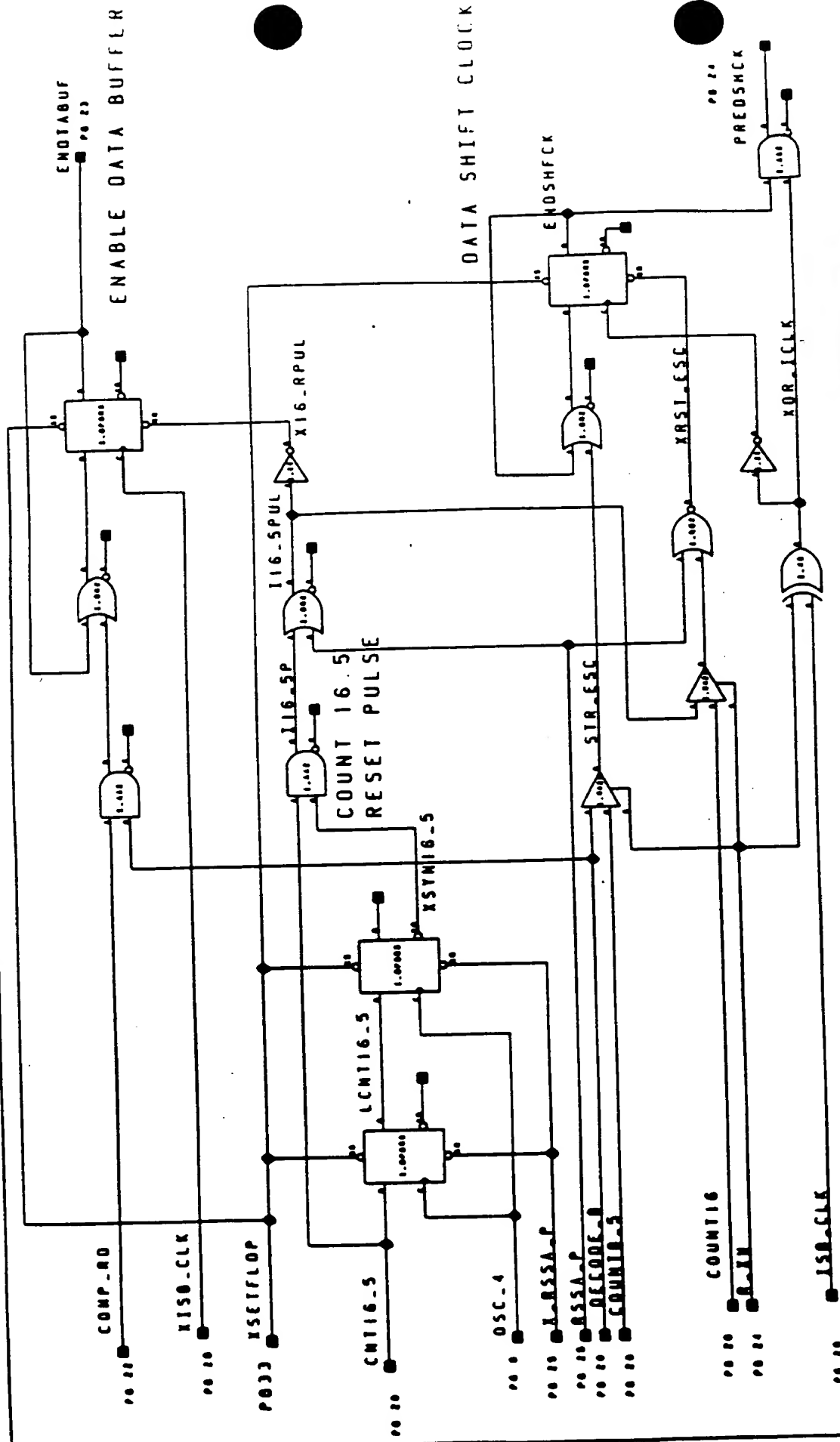




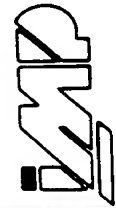
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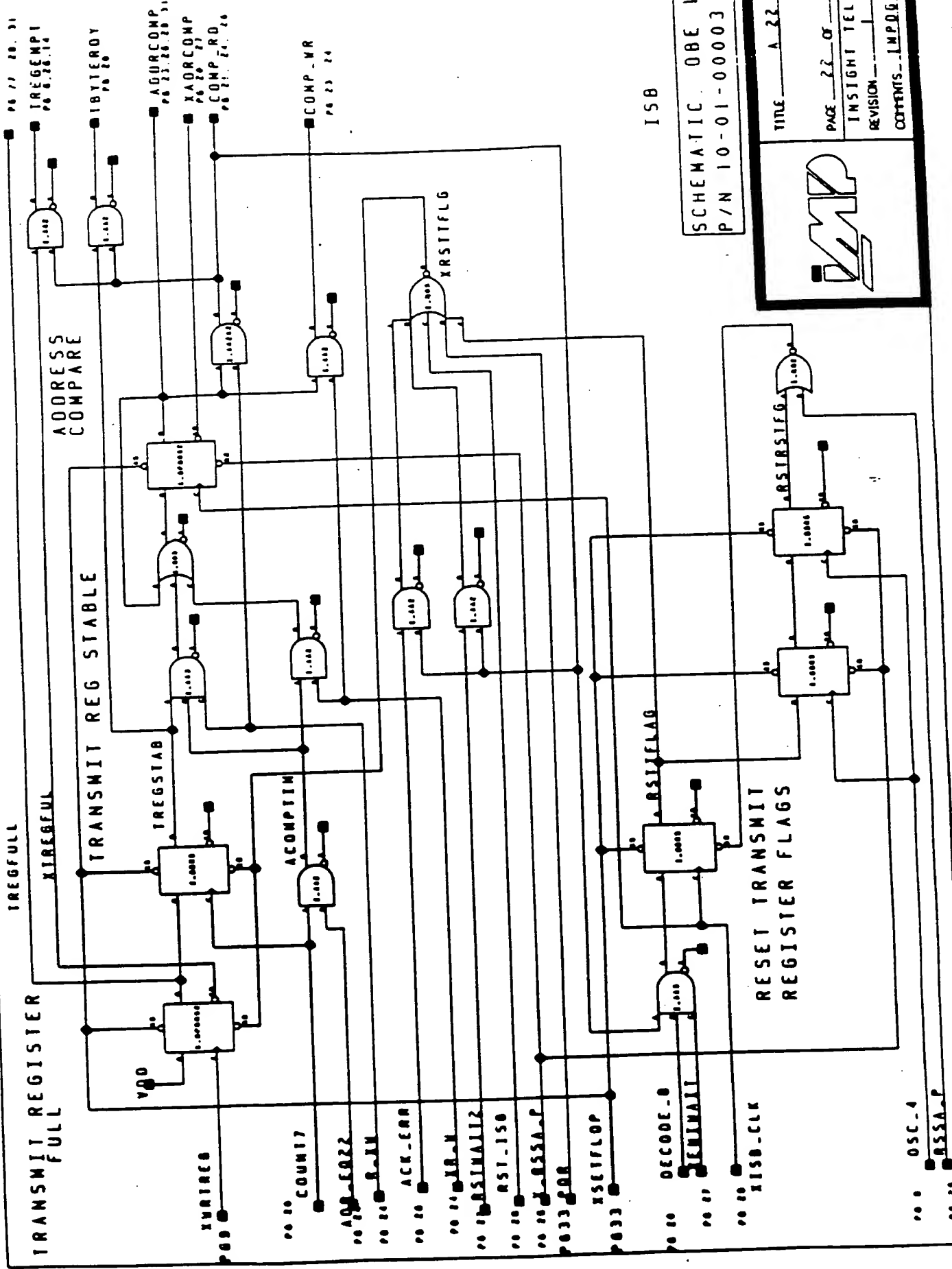


ISB



TIME A 21  
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REVISION  
CONTENTS IMPROVED

SCHEMATIC DBE 1200  
P/N 10-01-00003



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SCHEMATIC OBE 1200  
P/N 10-01-00003



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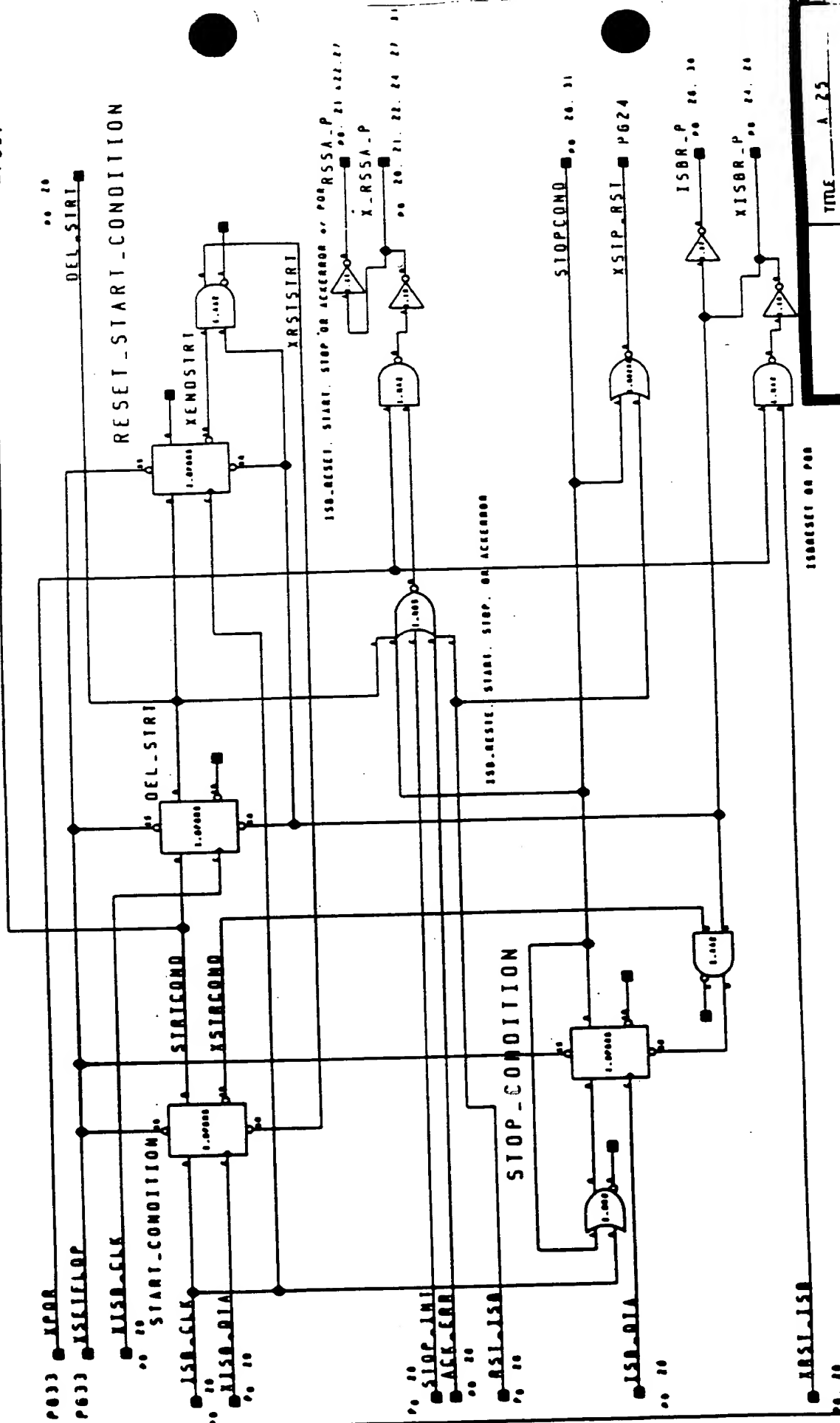






DEL-START-COND

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**FOR MORE INFORMATION**



11

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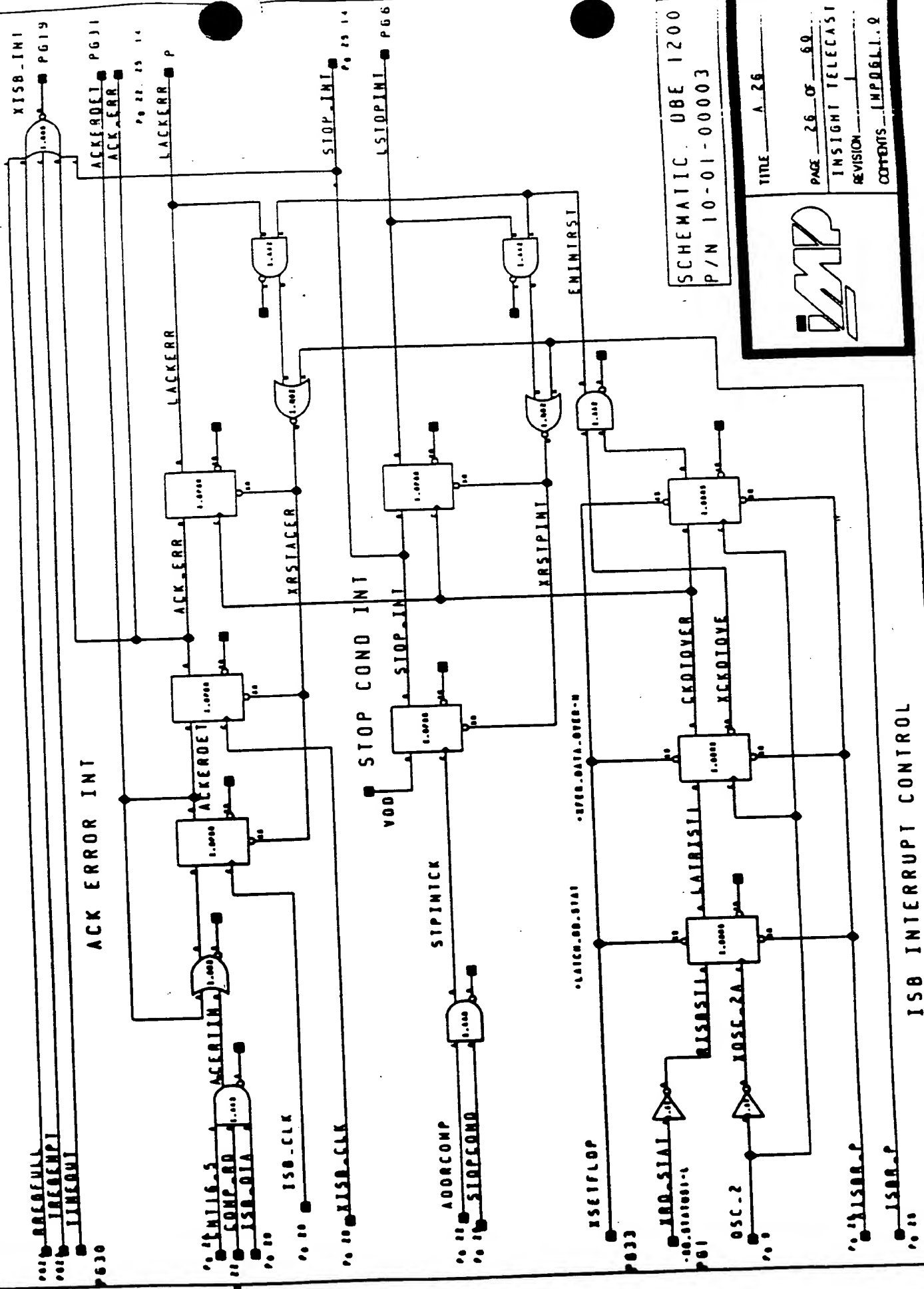
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INSIGHT TELECAST


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P/N 10-01-00003

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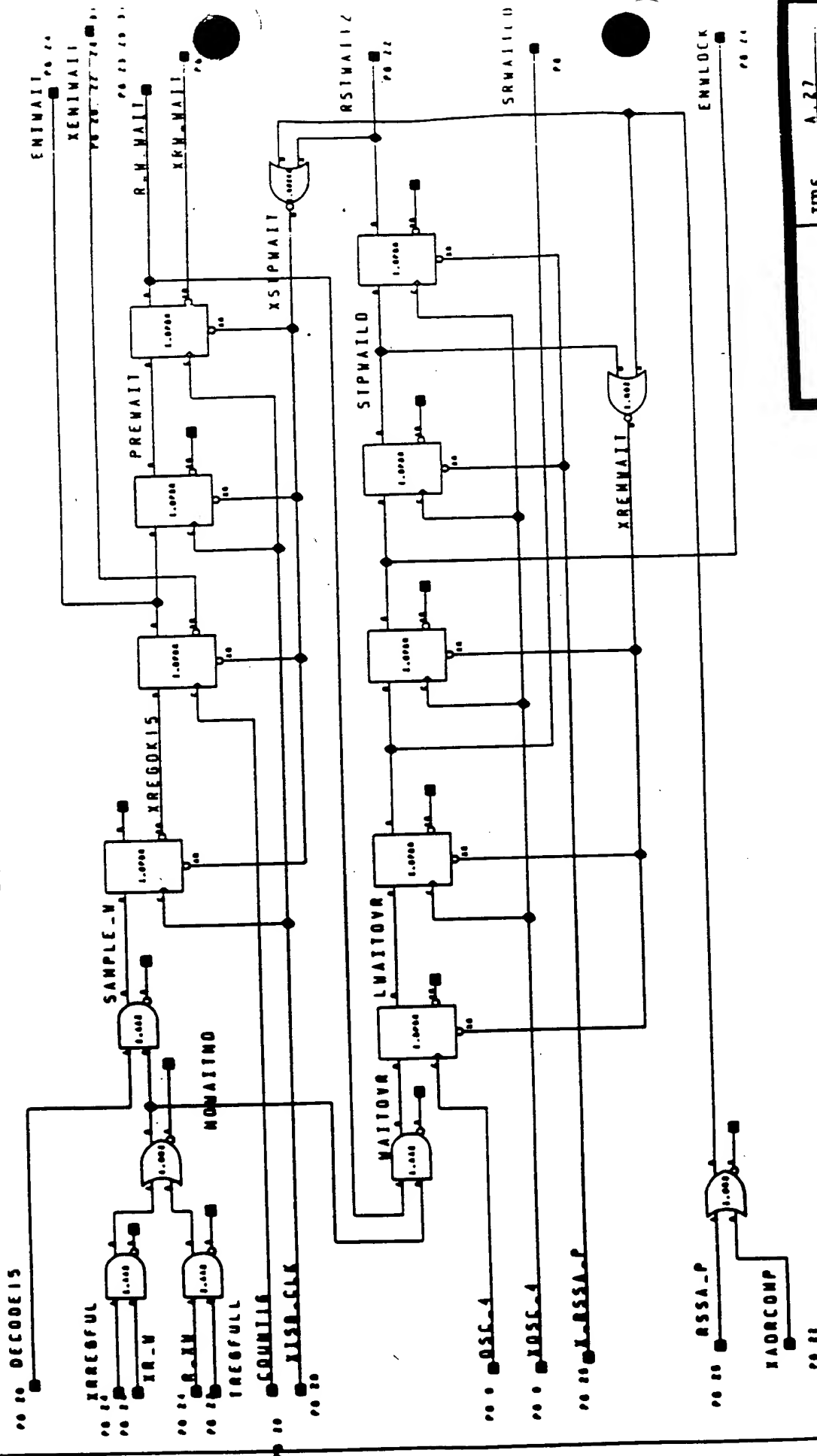


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ISB INTERRUPT CONTROL

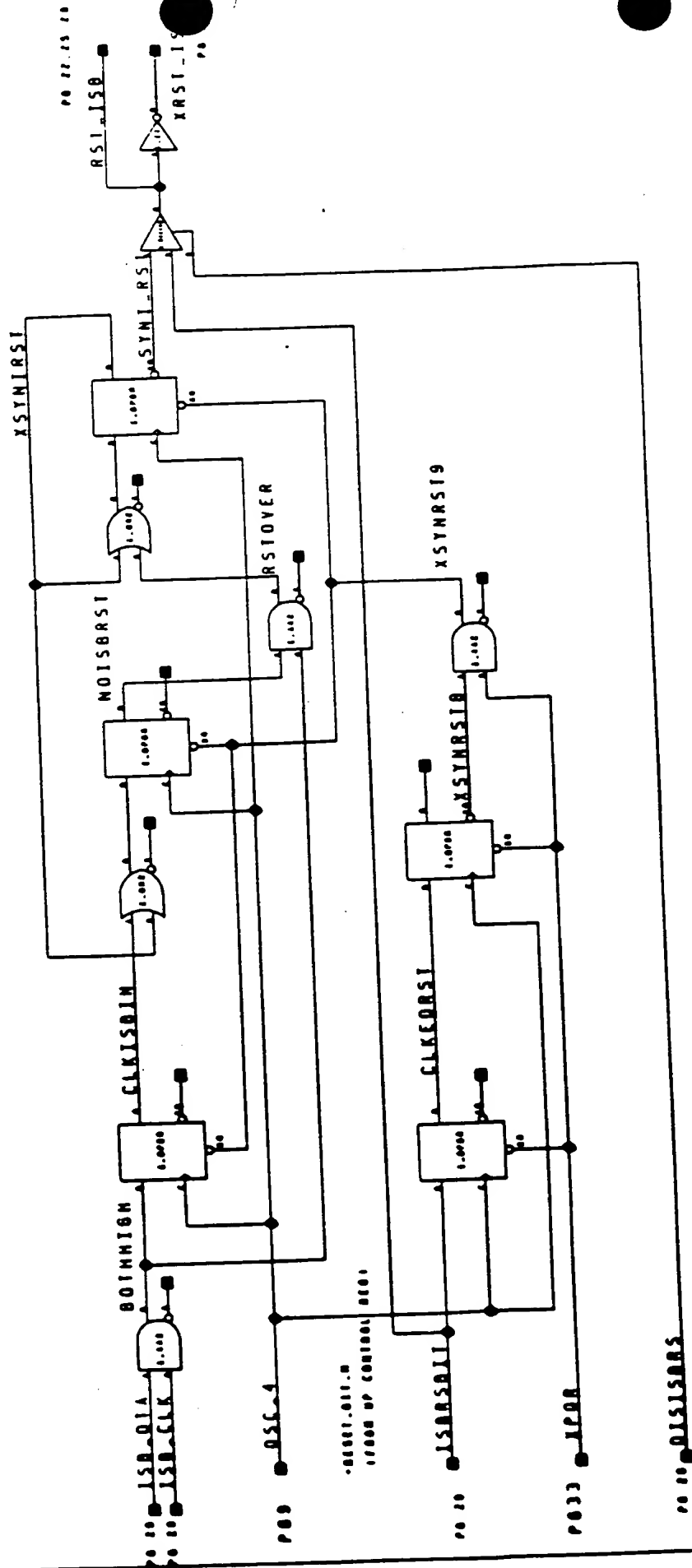
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**RESET SYNCHRONIZER**



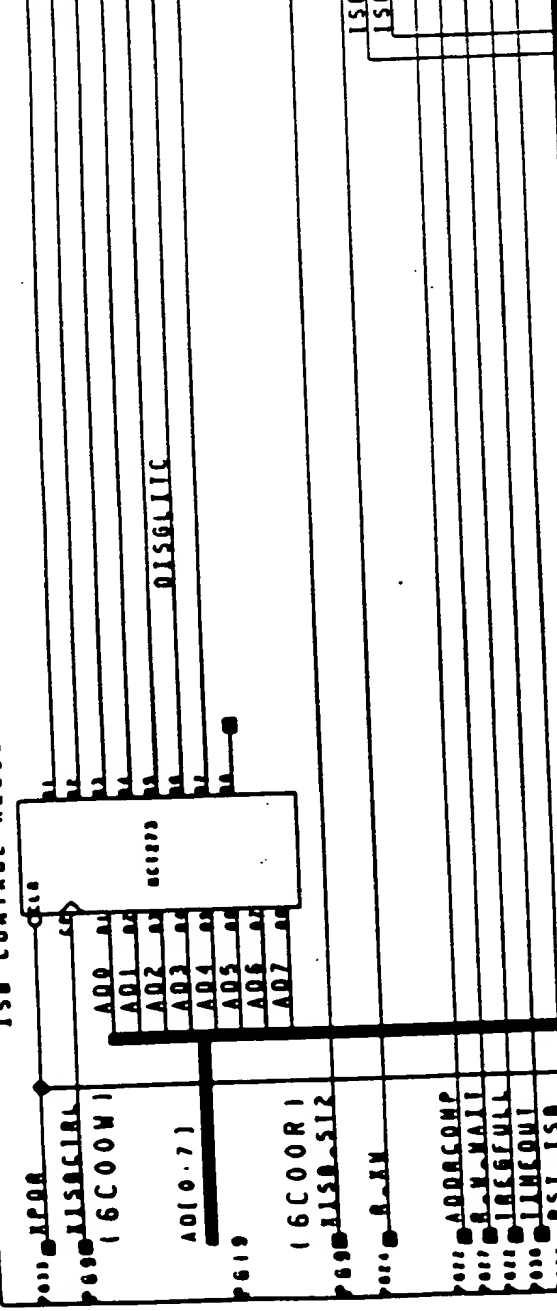
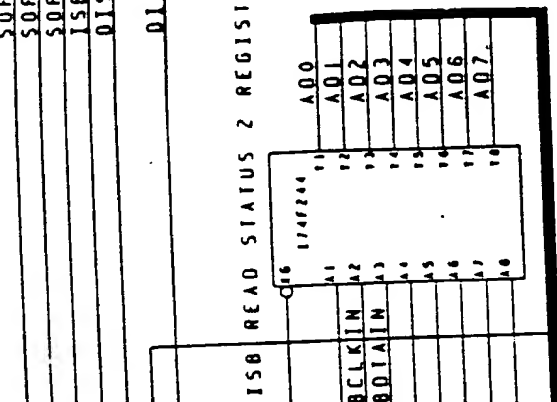
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# ISB CONTROL REGISTER (WRITE)

SOFT\_ISB PG23  
 SOFT\_CLK PG21  
 SOFT\_DIA PG21  
 ISB5011 PG23  
 DISISBRS PG28  
 DIS\_IQV1 PG30



ISB\_CLK PG19, 20, 21  
 24, 25, 26, 28  
 XISB\_CLK PG19, 20, 21  
 22, 25, 26, 27

ISB\_DIA PG24, 25  
 26, 27  
 XISB\_DIA PG25

## ISB CLOCK DEGLITCHERS

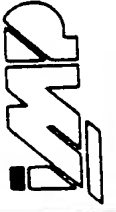
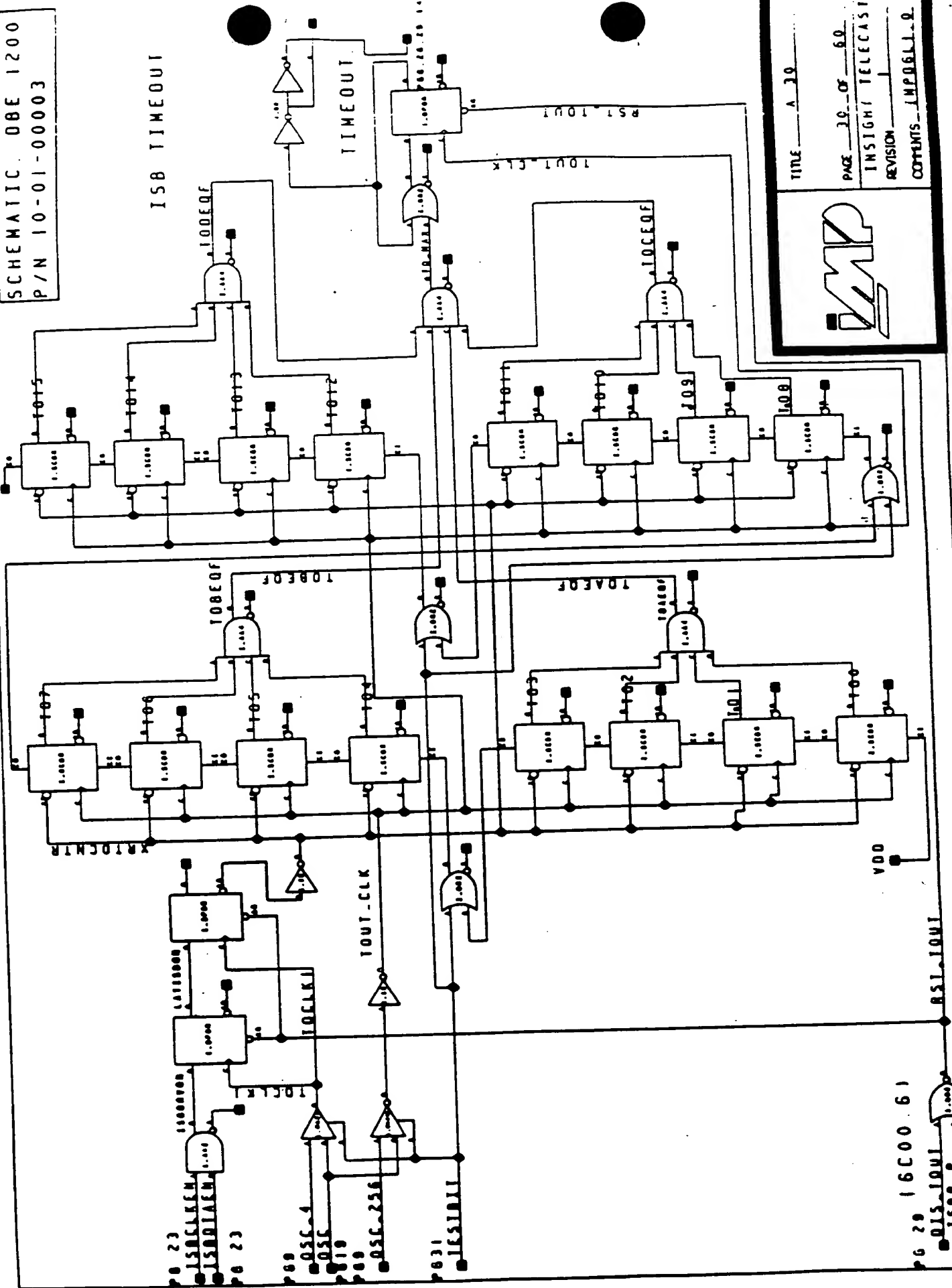
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ISB TIMEOUT

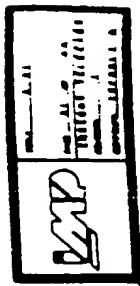


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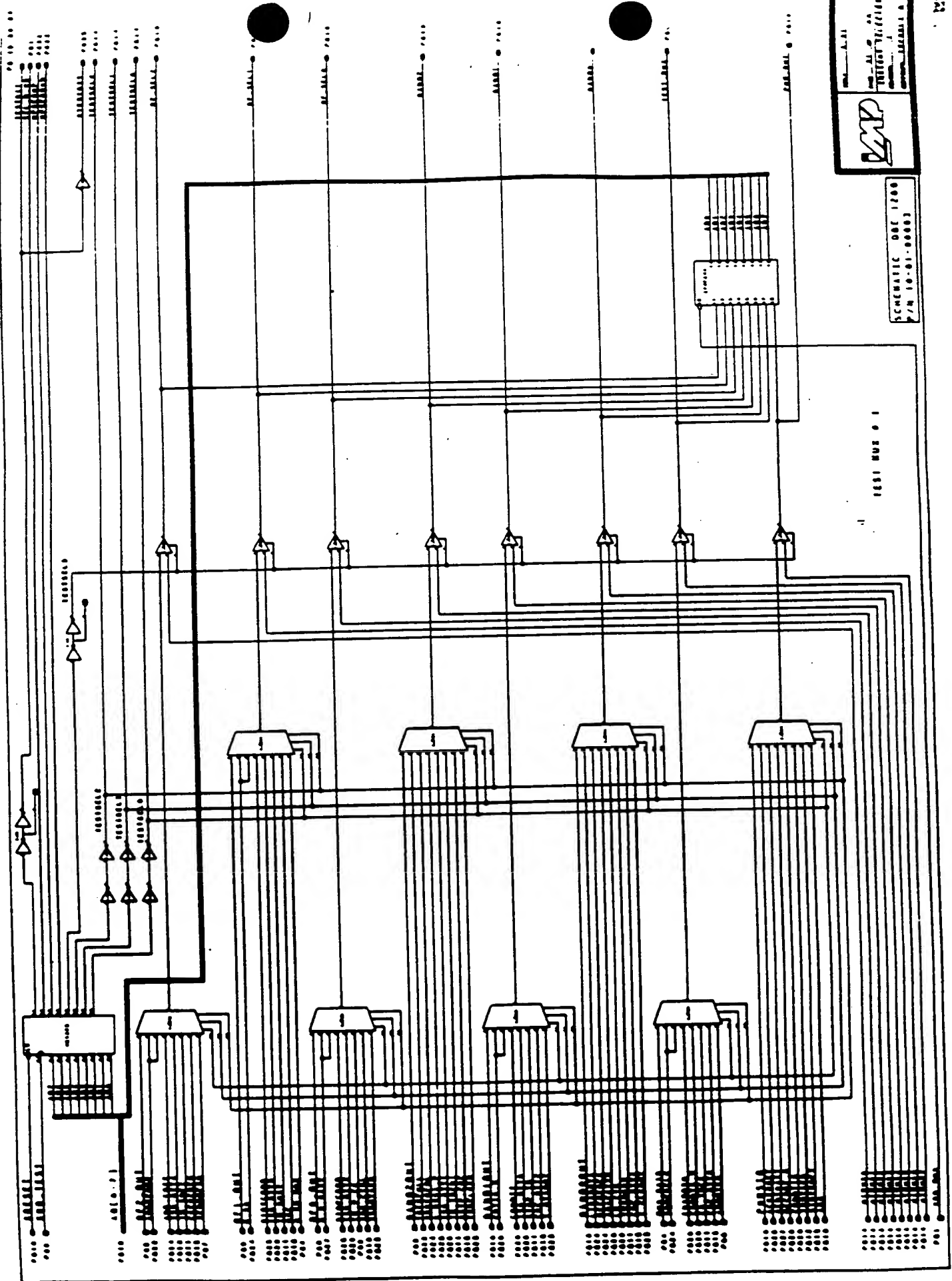
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1500-P

1. This schematic is for the test equipment used in the test of the aircraft engine. It is not to be used for any other purpose.



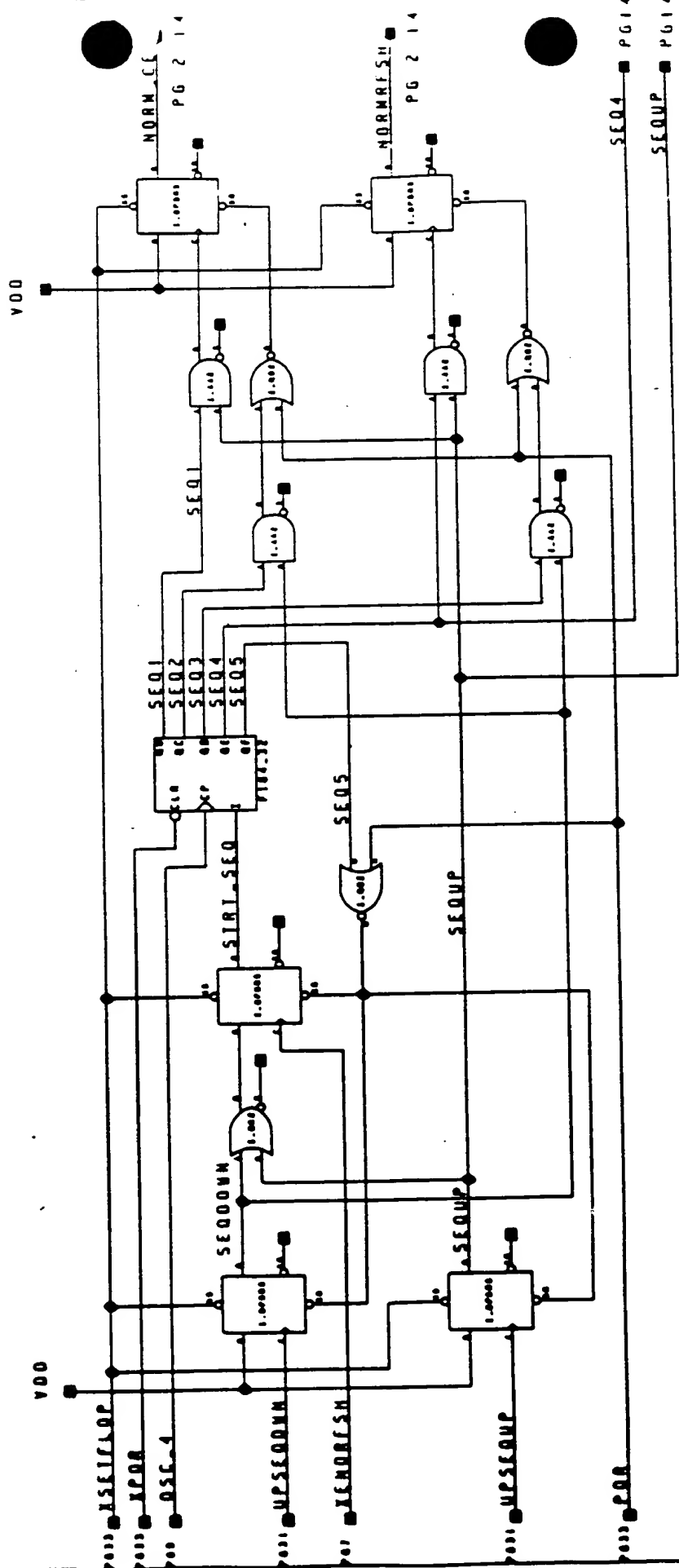
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TEST BUS 0 1

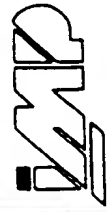




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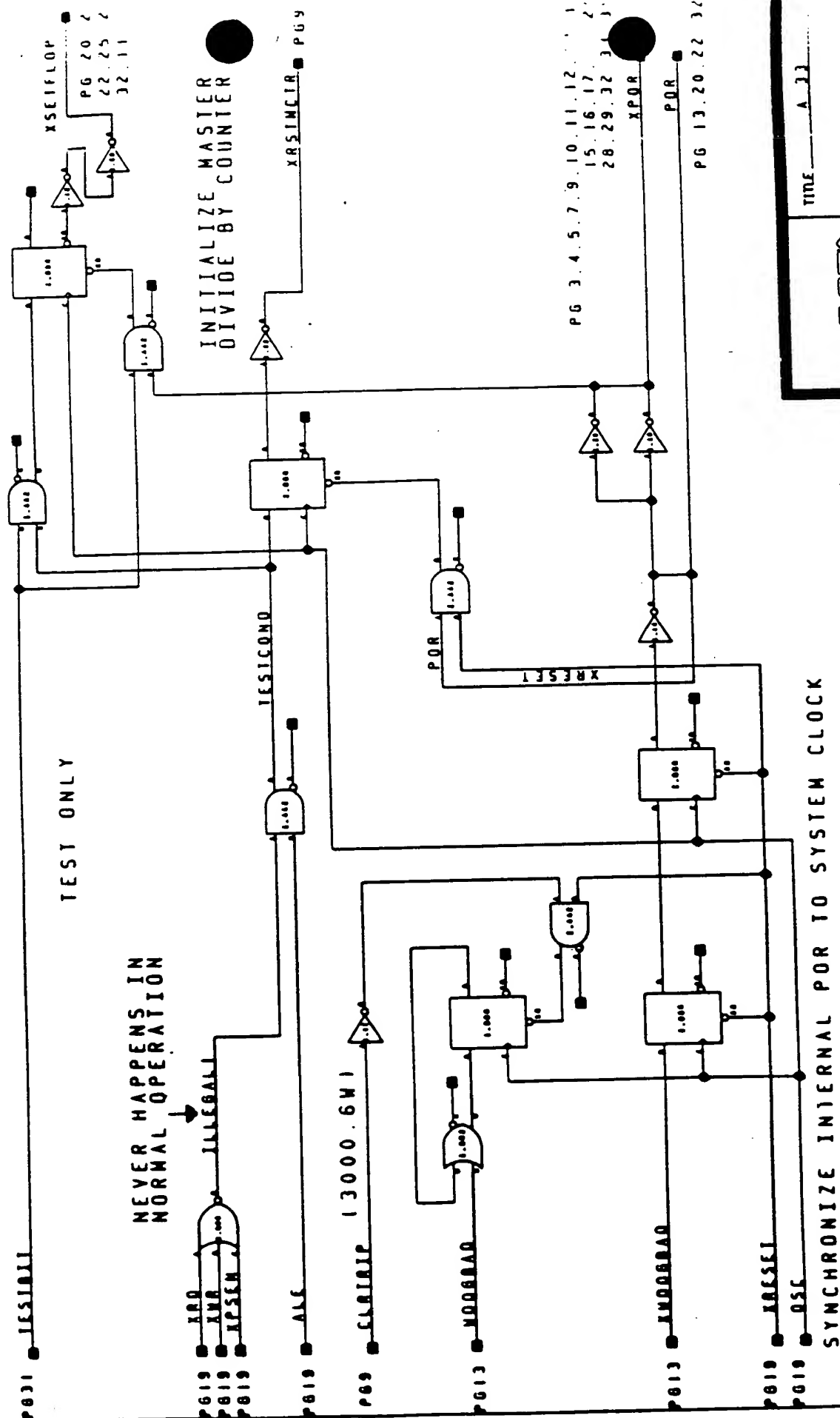
PSRAM SEQUENCE UP/DOWN LOGIC



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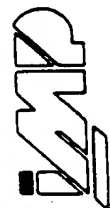
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NEVER HAPPENS IN  
NORMAL OPERATION



SYNCHRONIZE INTERNAL POR TO SYSTEM CLOCK

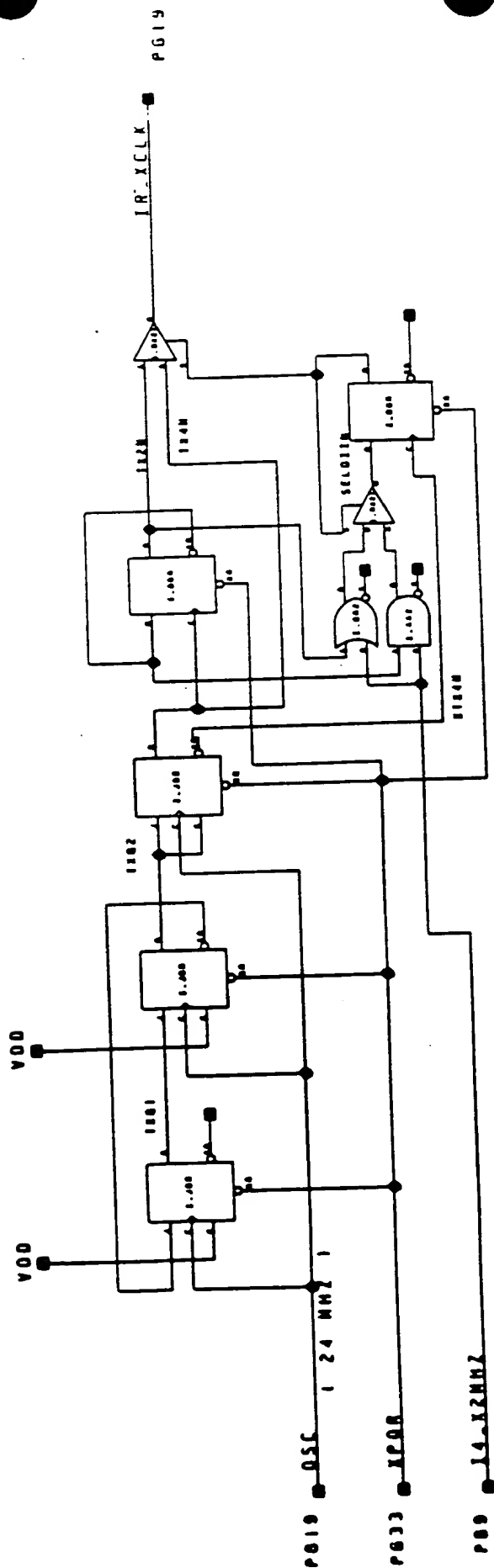
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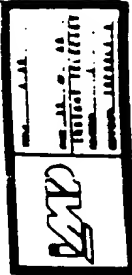
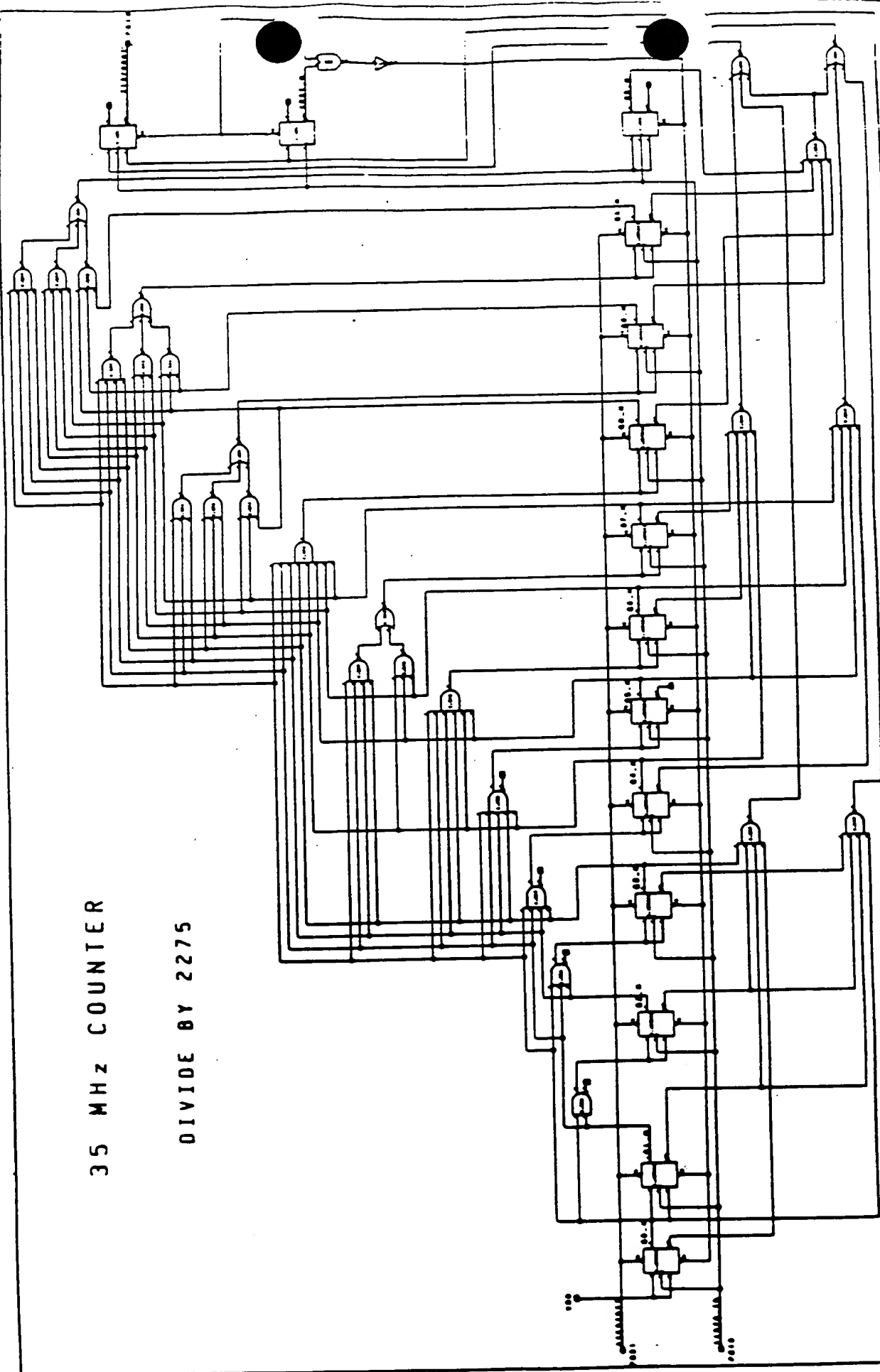


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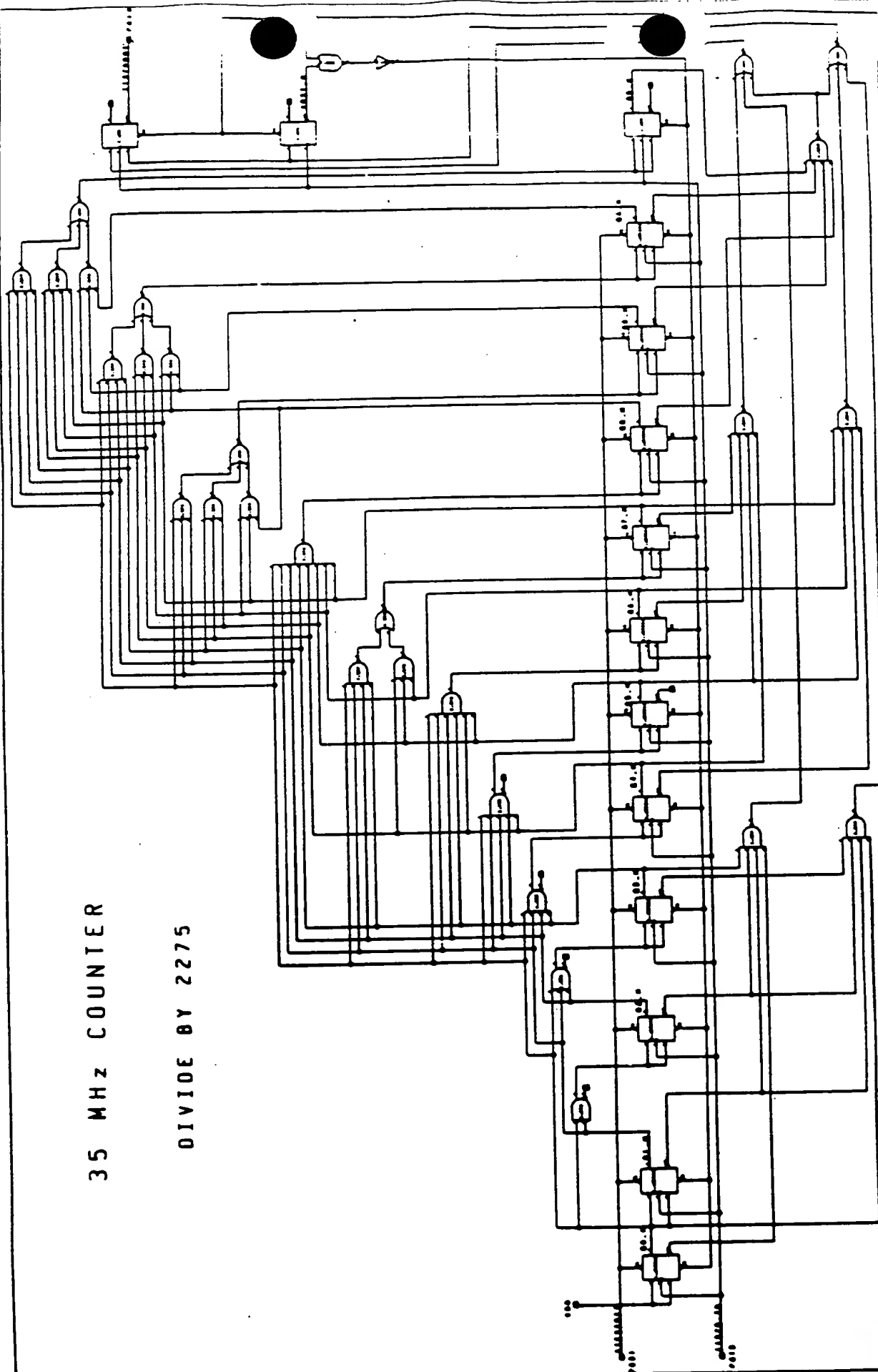


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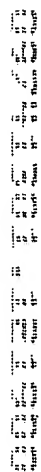
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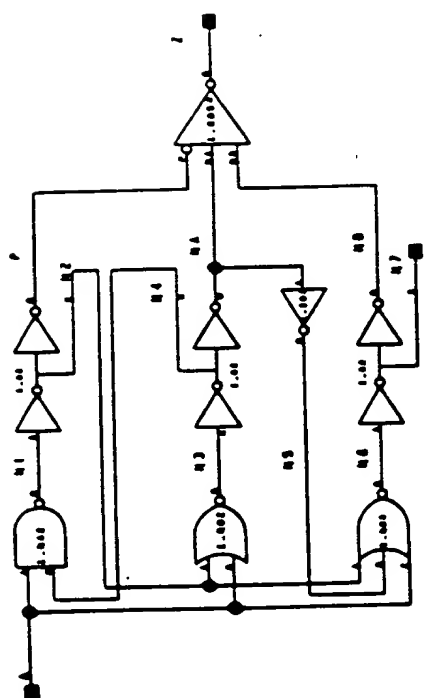
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 11. Material  
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 15. Assembly  
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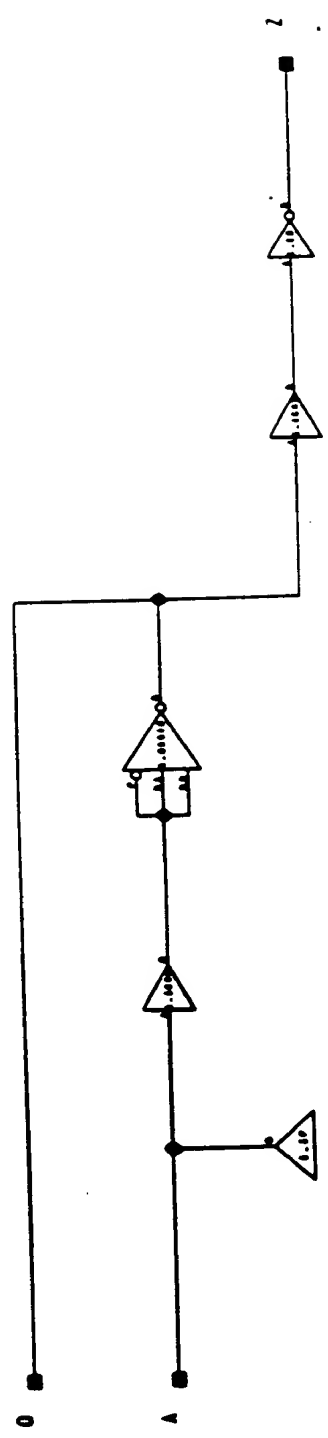


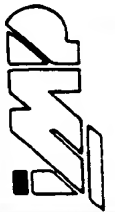
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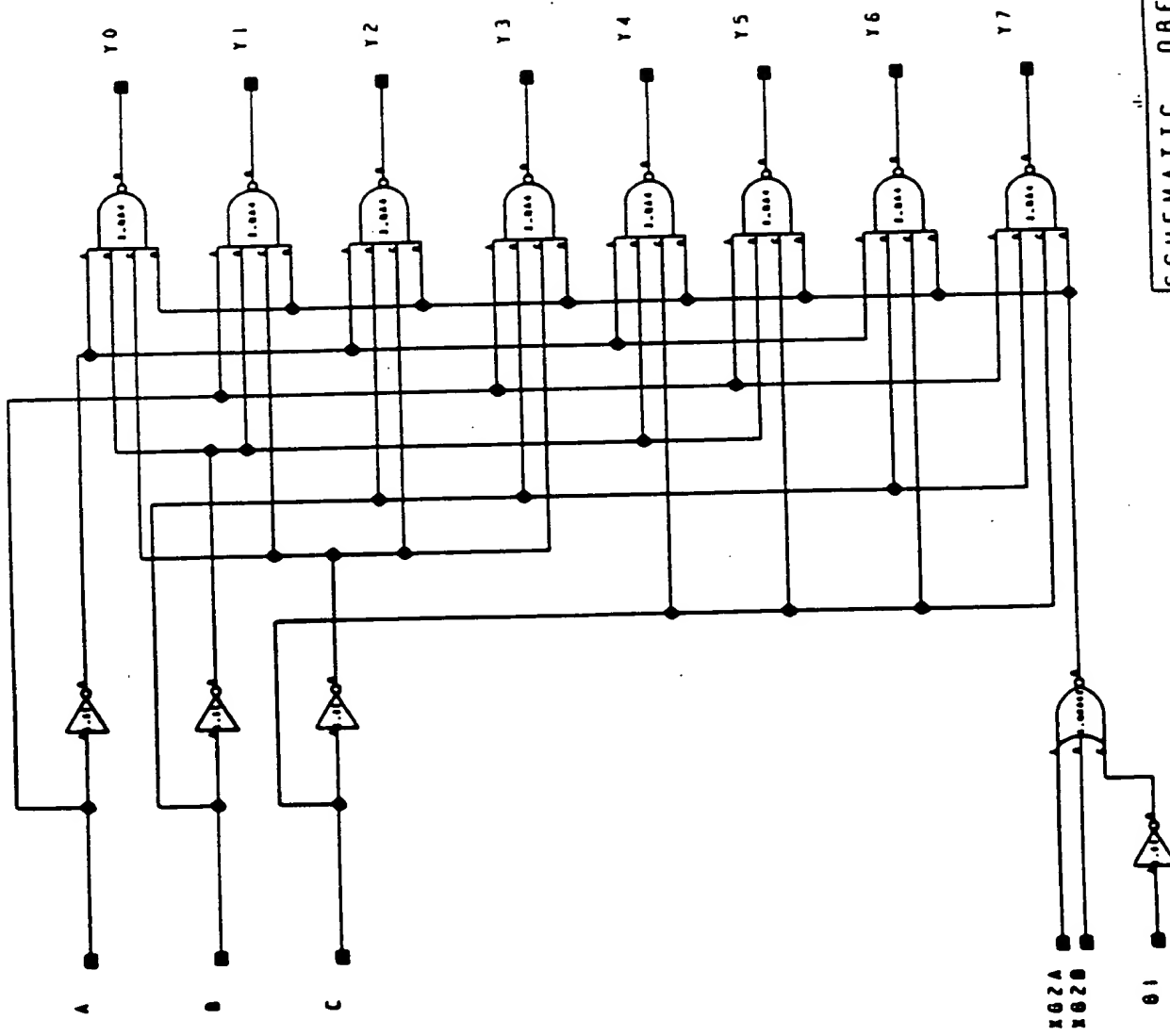
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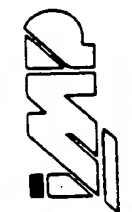


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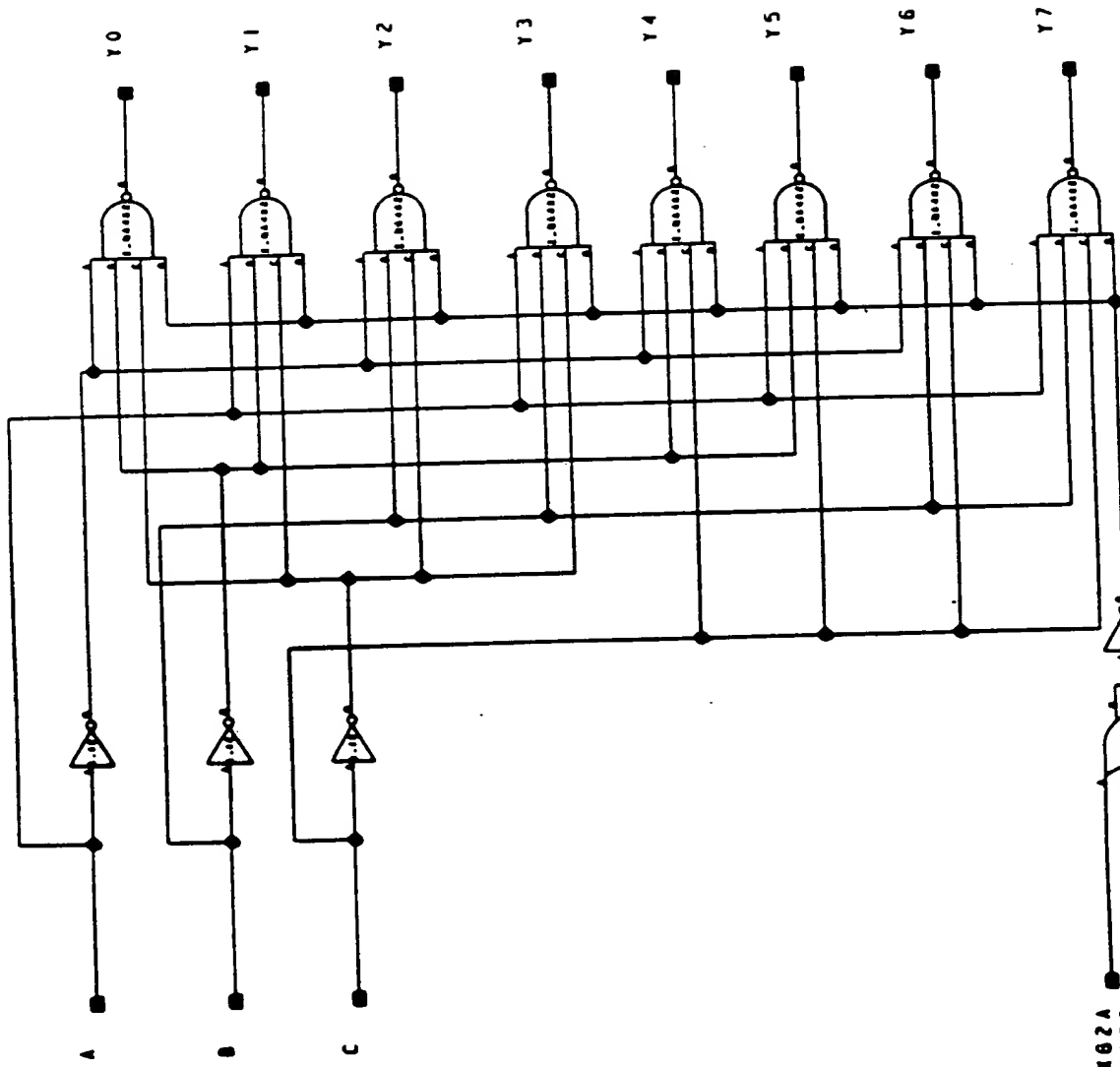
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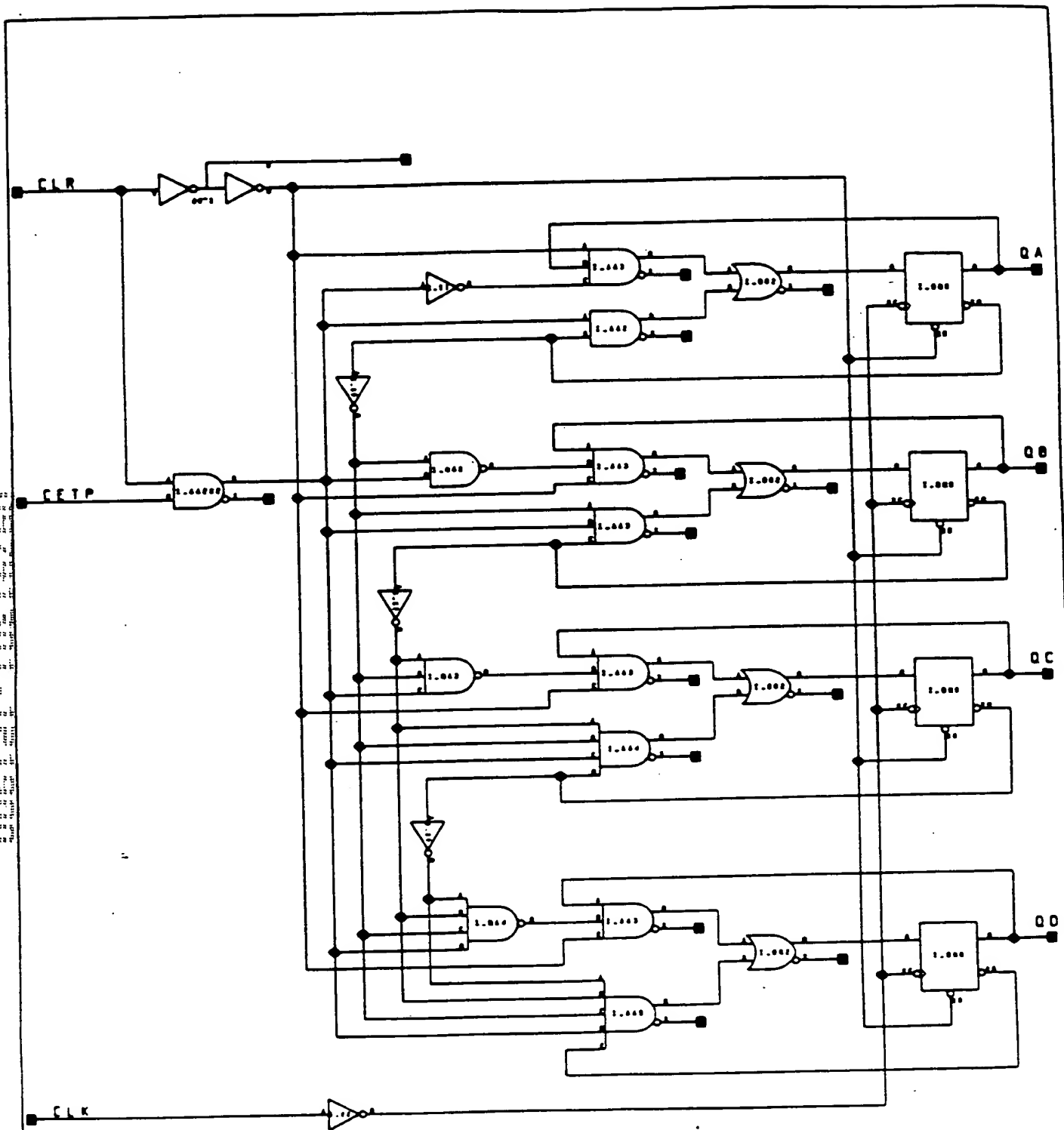
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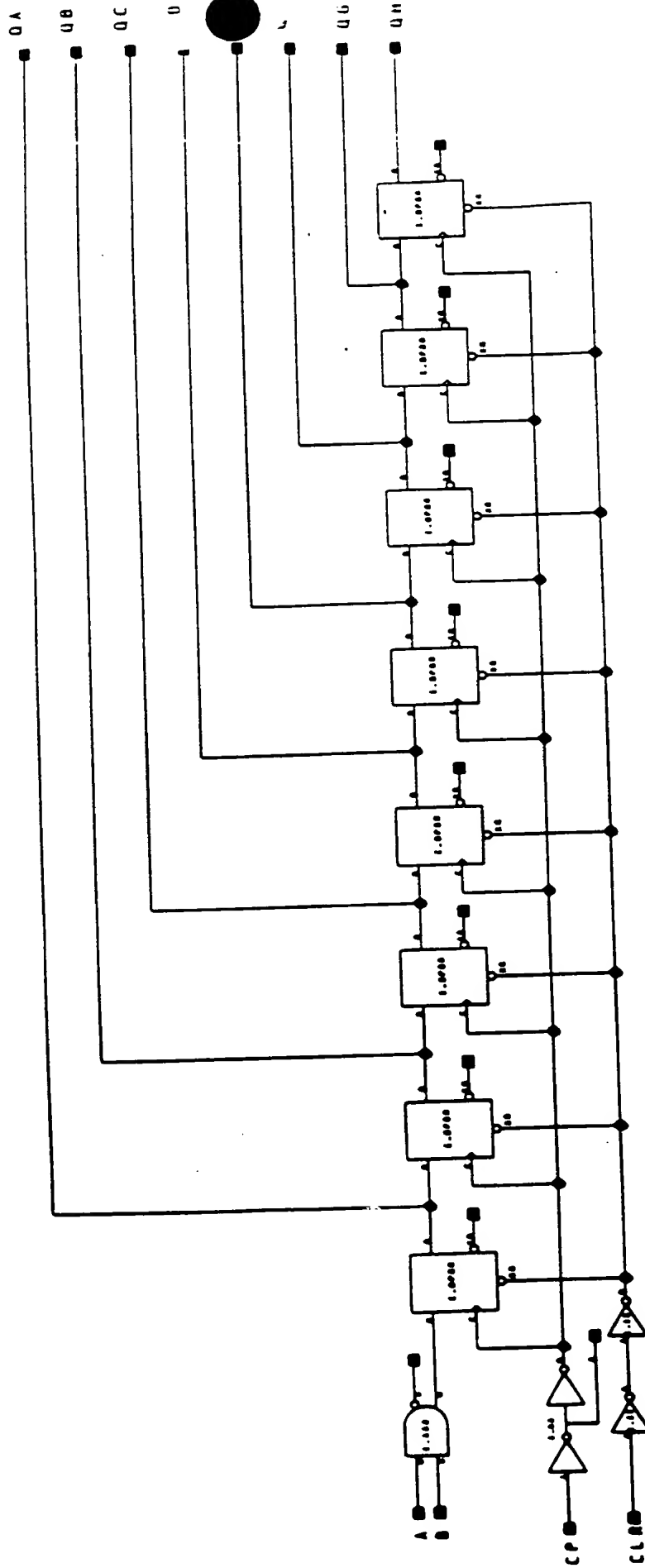
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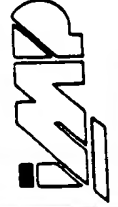
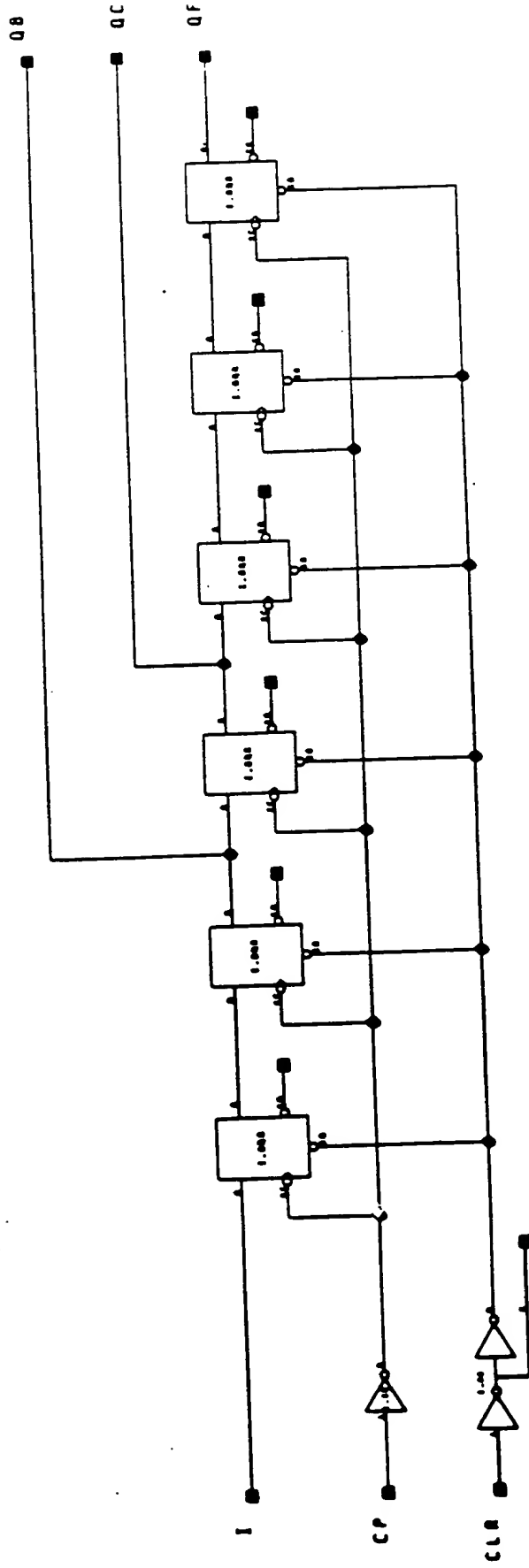
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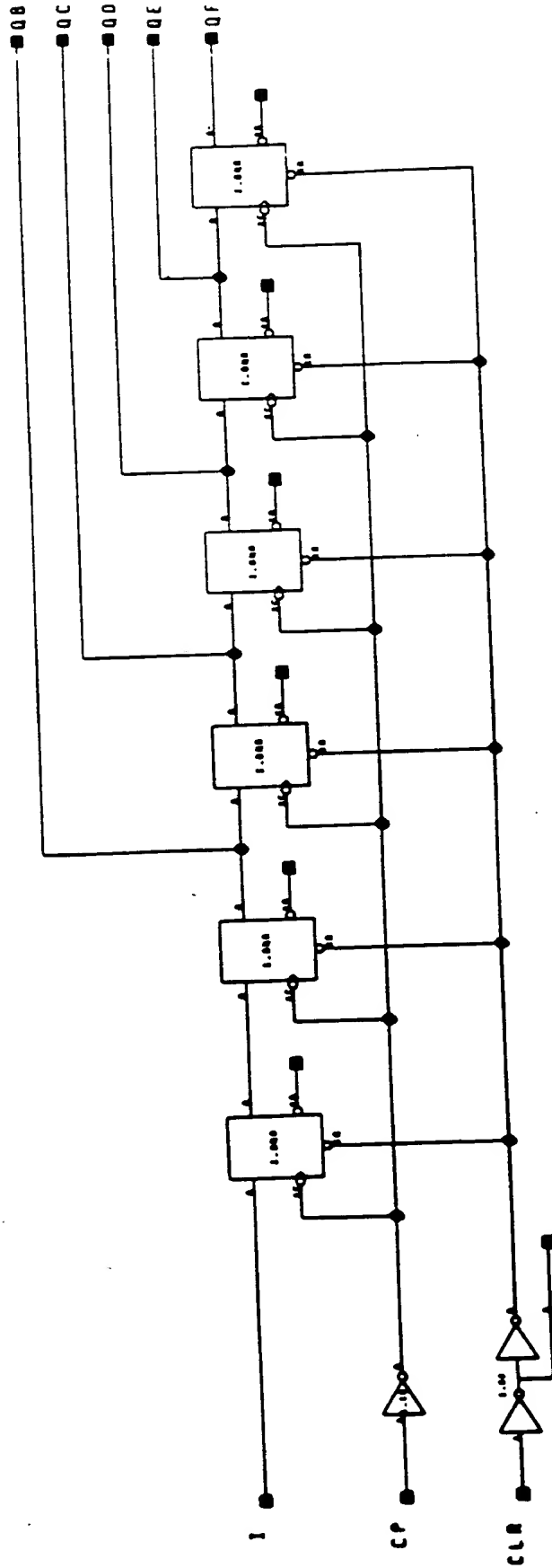


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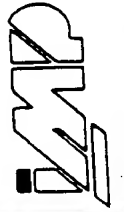
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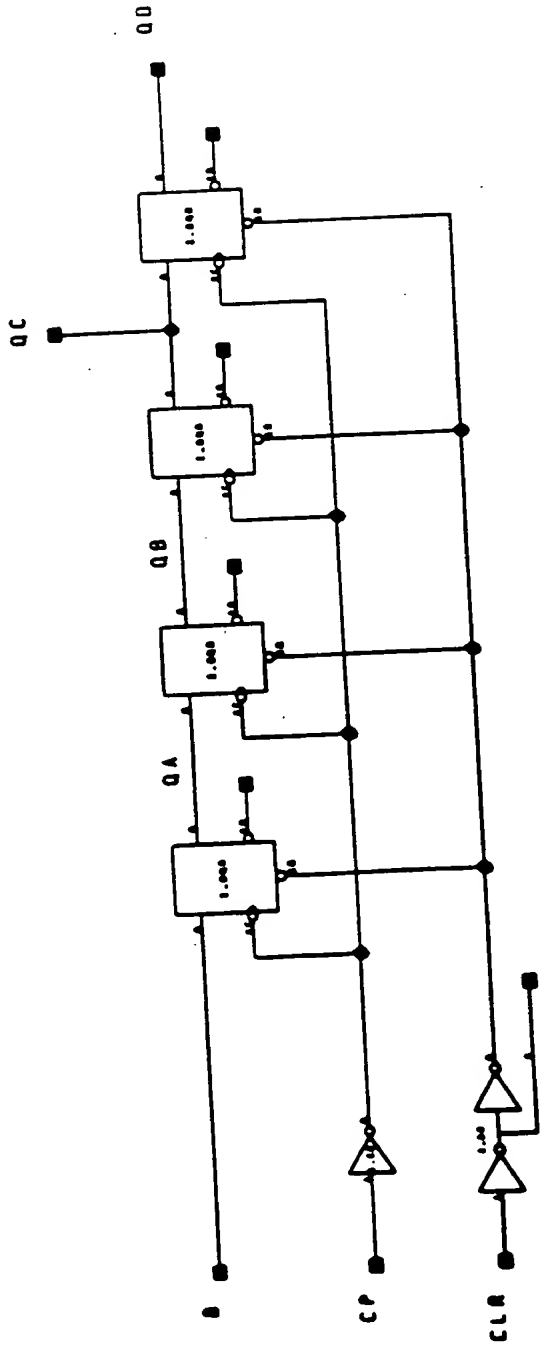


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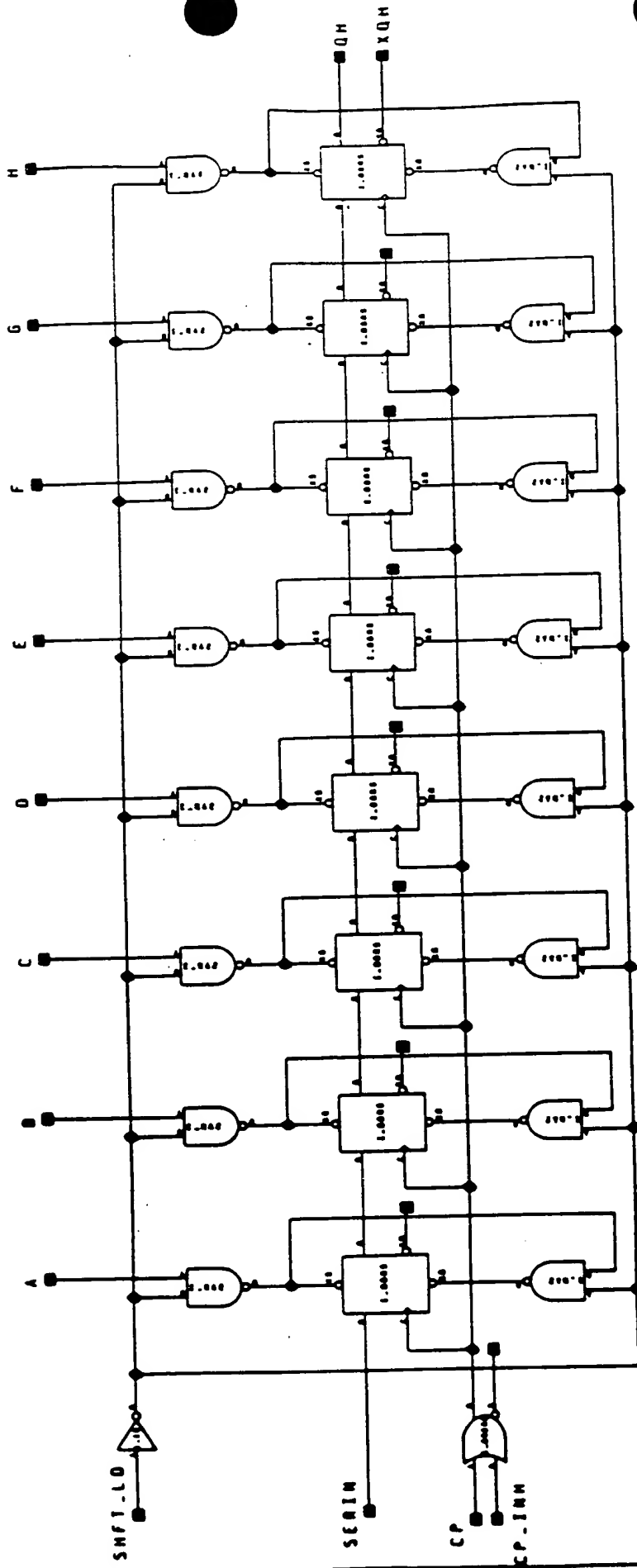
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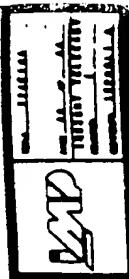
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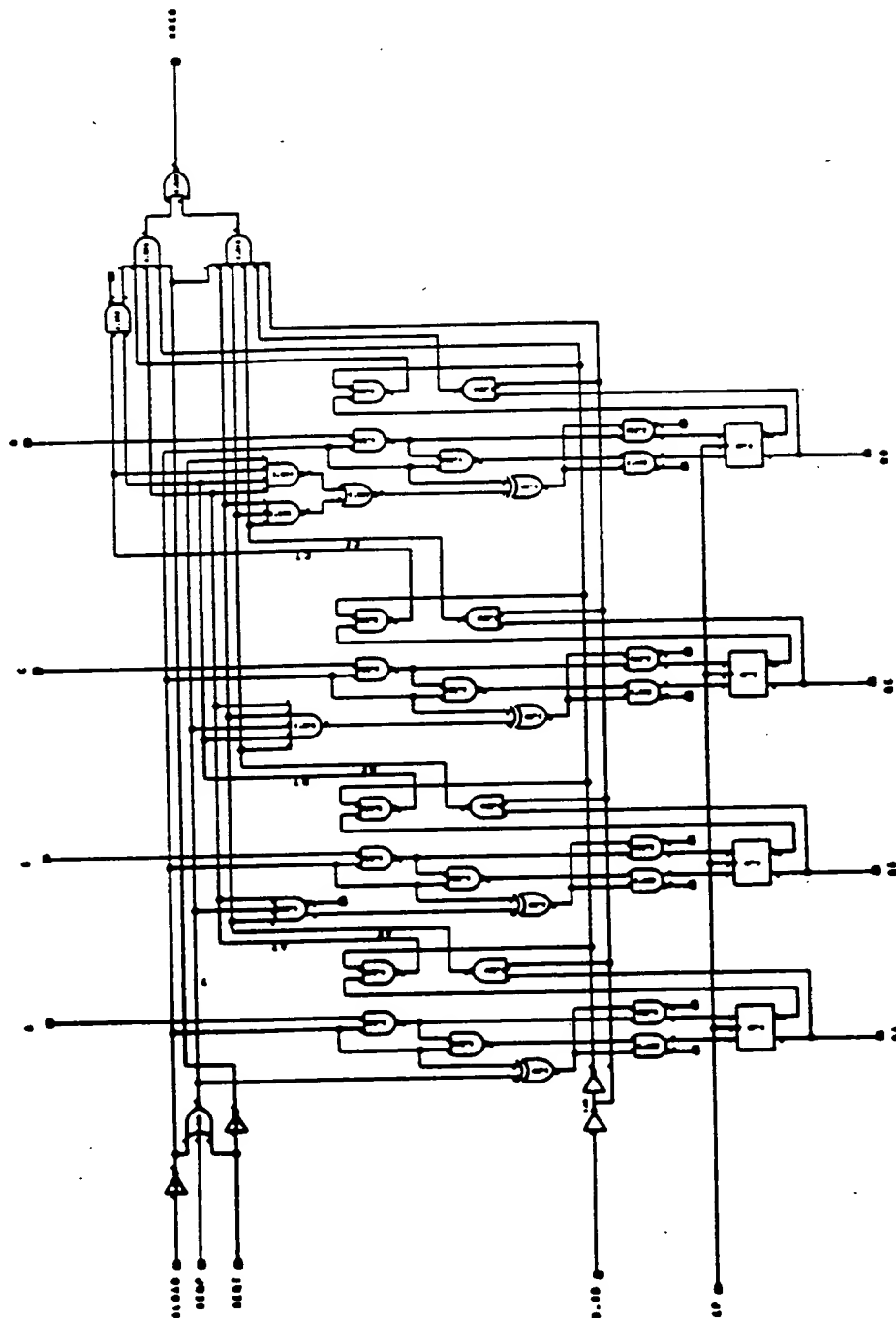


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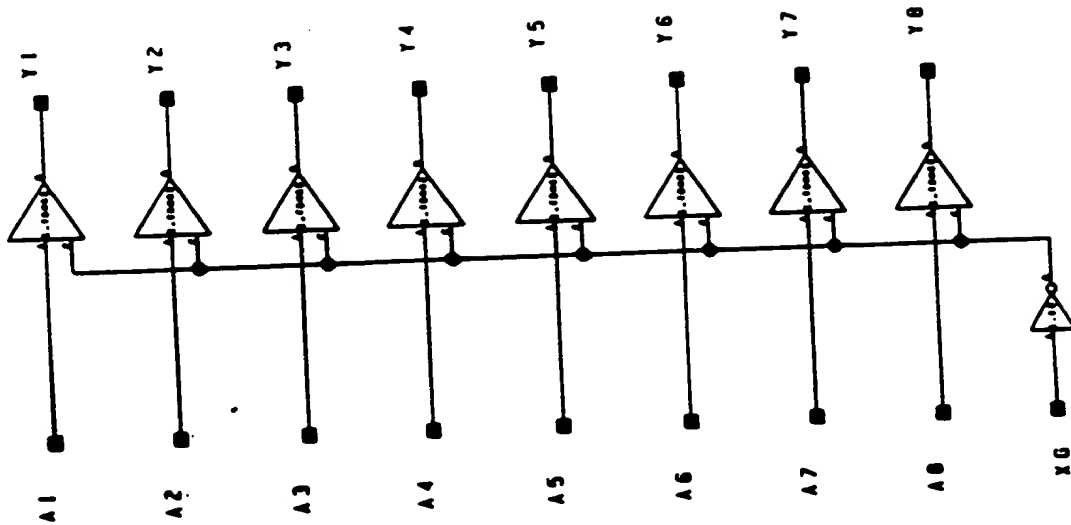
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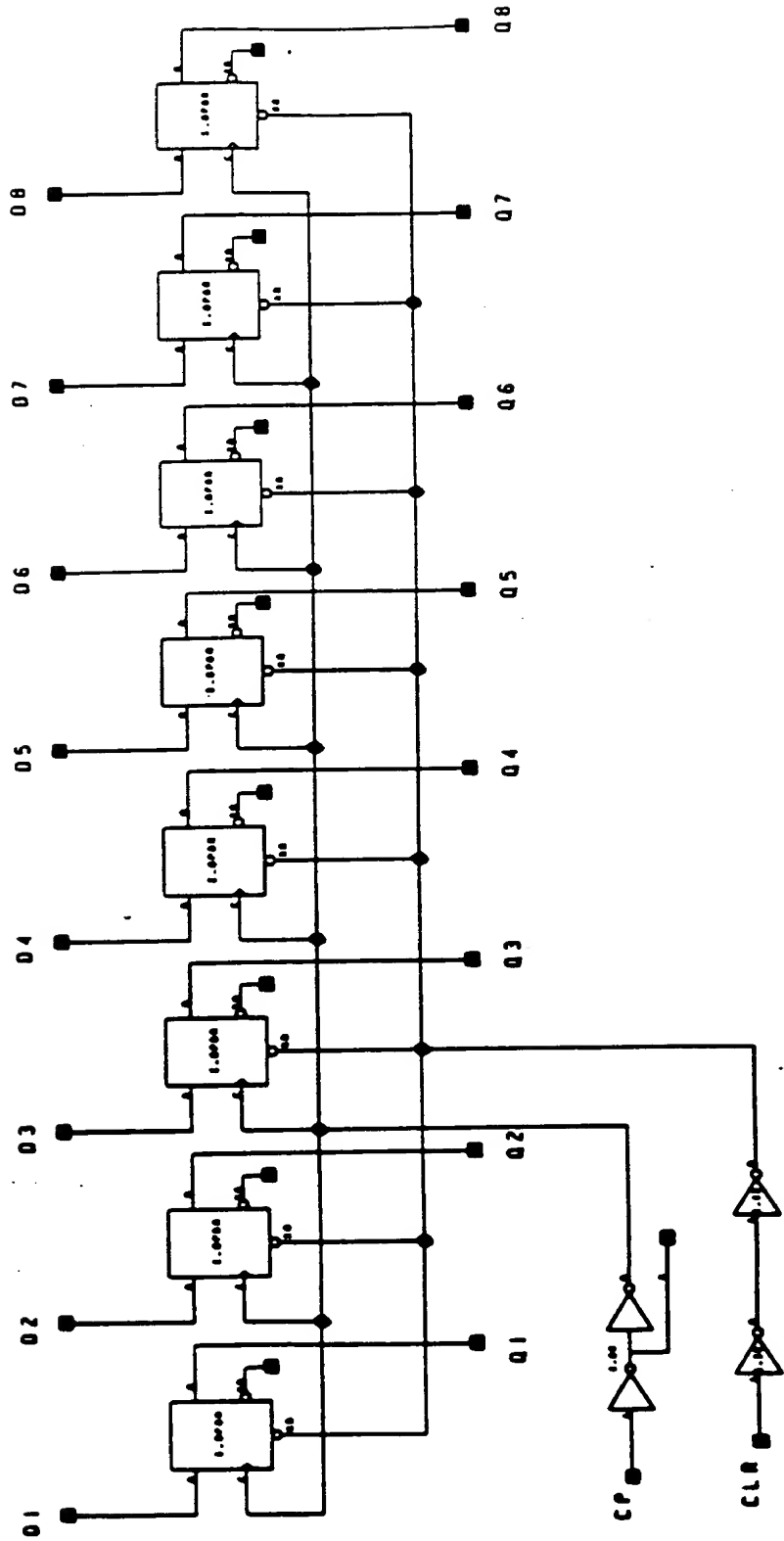
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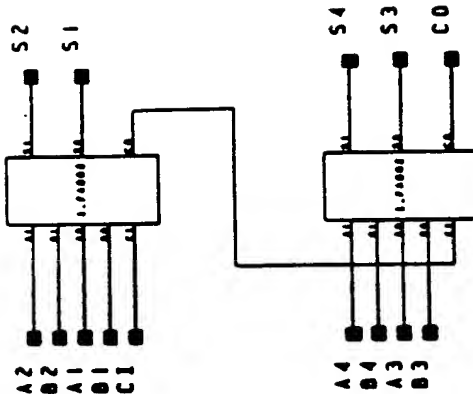


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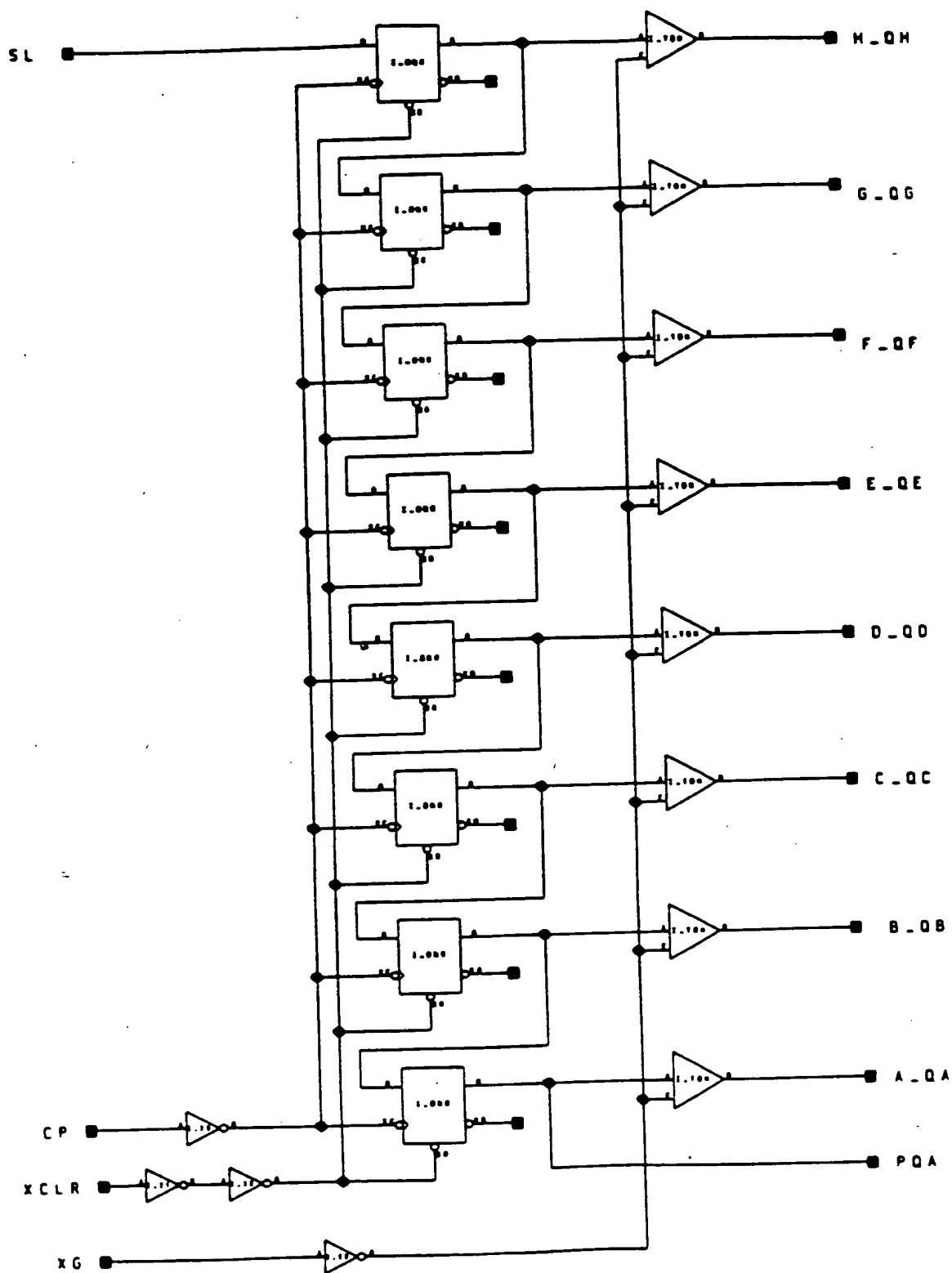
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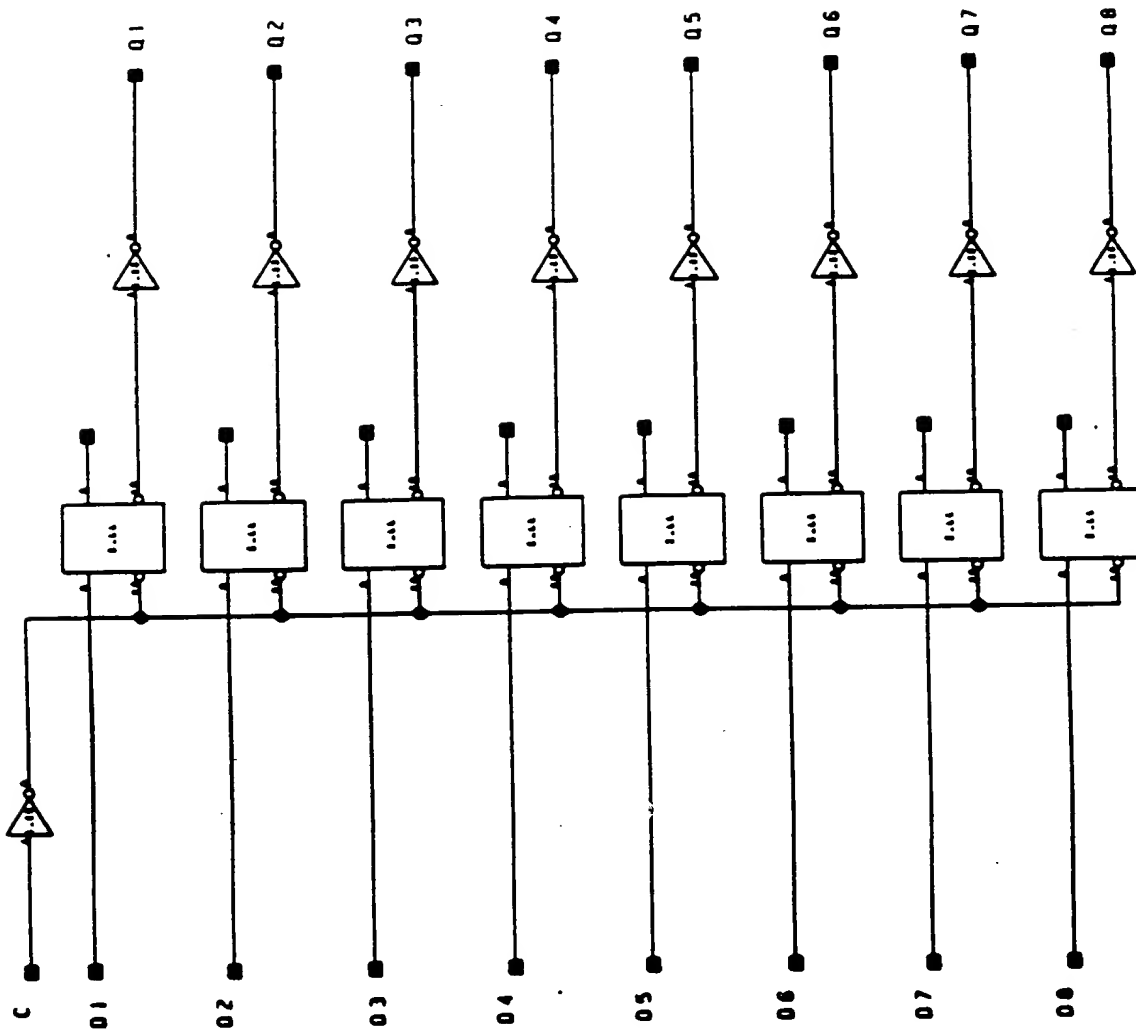


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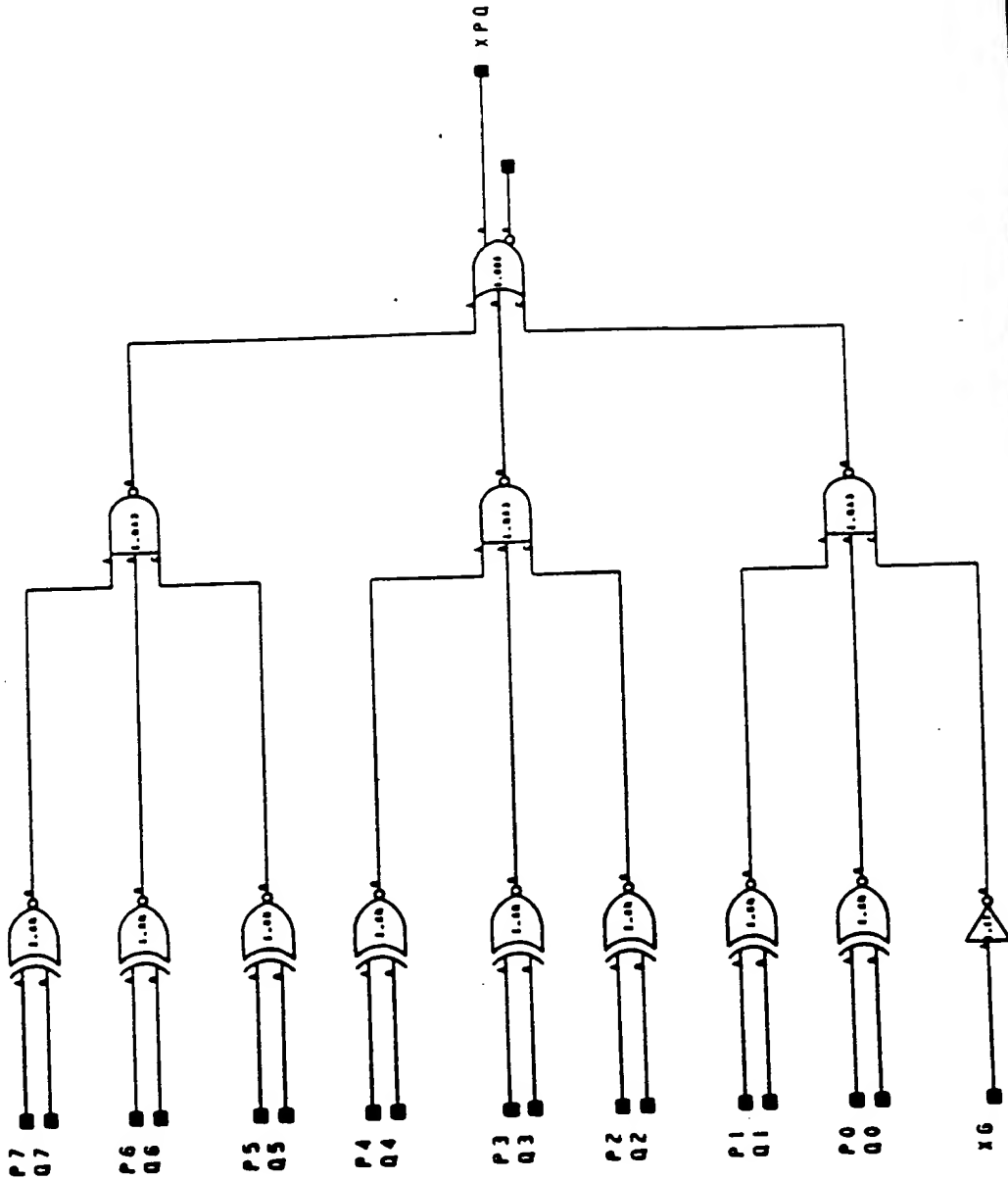
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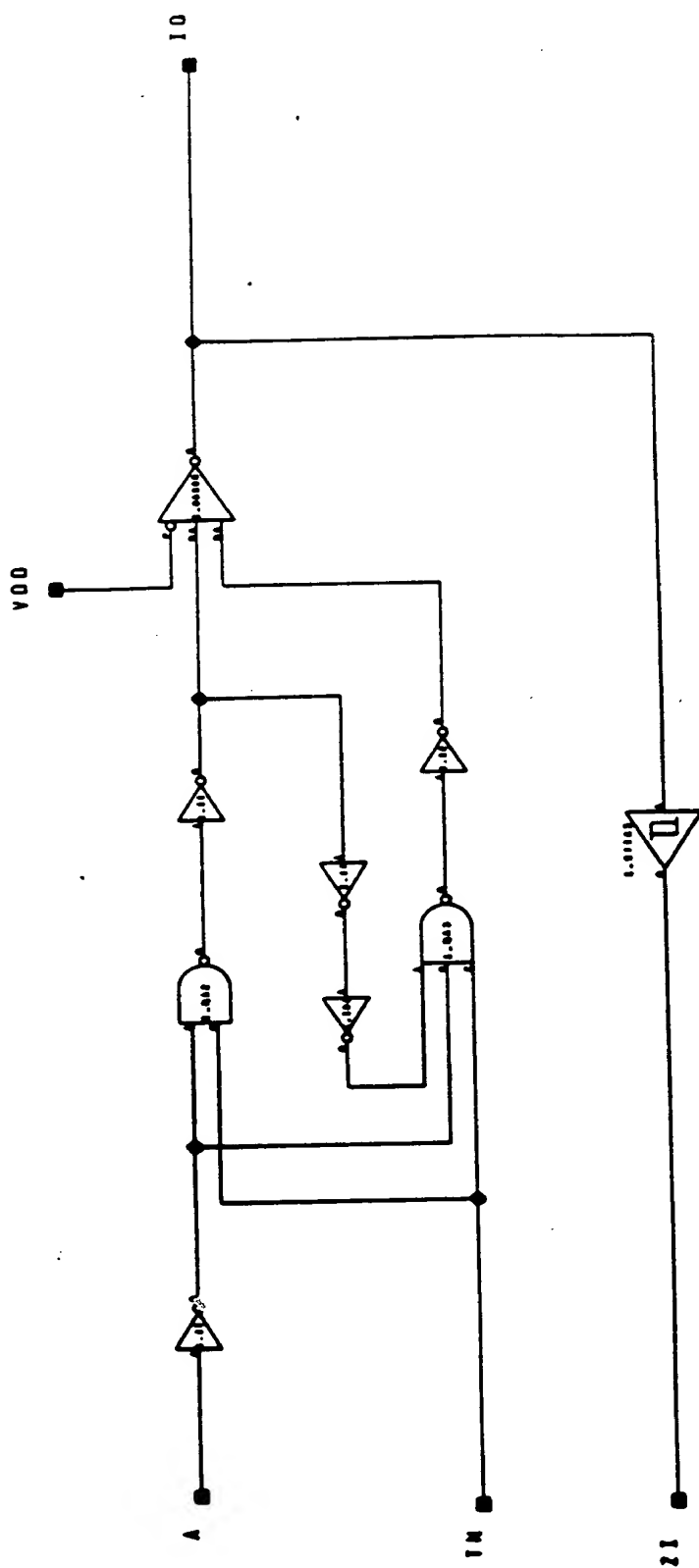
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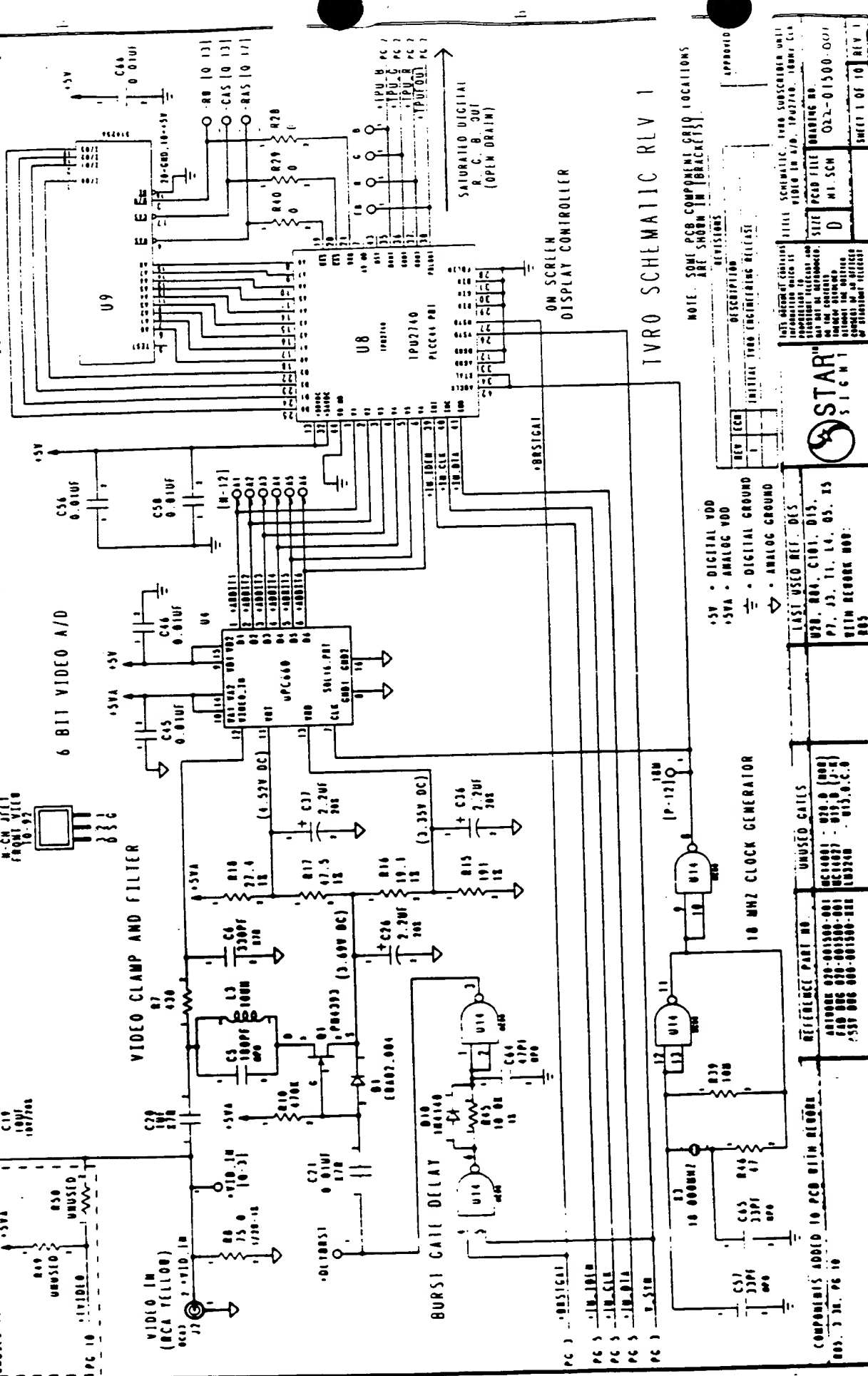
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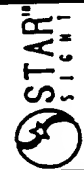


TVRO SCHEMATIC RLV 1

NOTE: SOME PCB COMPONENT GRID LOCATIONS ARE SHOWN IN BRACKET.

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| 1   |      | INITIAL TVRO ENGINEERING RELEASE |         |      |           |
| 2   |      | REVISIONS                        |         |      |           |
| 3   |      | REVISIONS                        |         |      |           |
| 4   |      | REVISIONS                        |         |      |           |
| 5   |      | REVISIONS                        |         |      |           |
| 6   |      | REVISIONS                        |         |      |           |
| 7   |      | REVISIONS                        |         |      |           |
| 8   |      | REVISIONS                        |         |      |           |
| 9   |      | REVISIONS                        |         |      |           |
| 10  |      | REVISIONS                        |         |      |           |

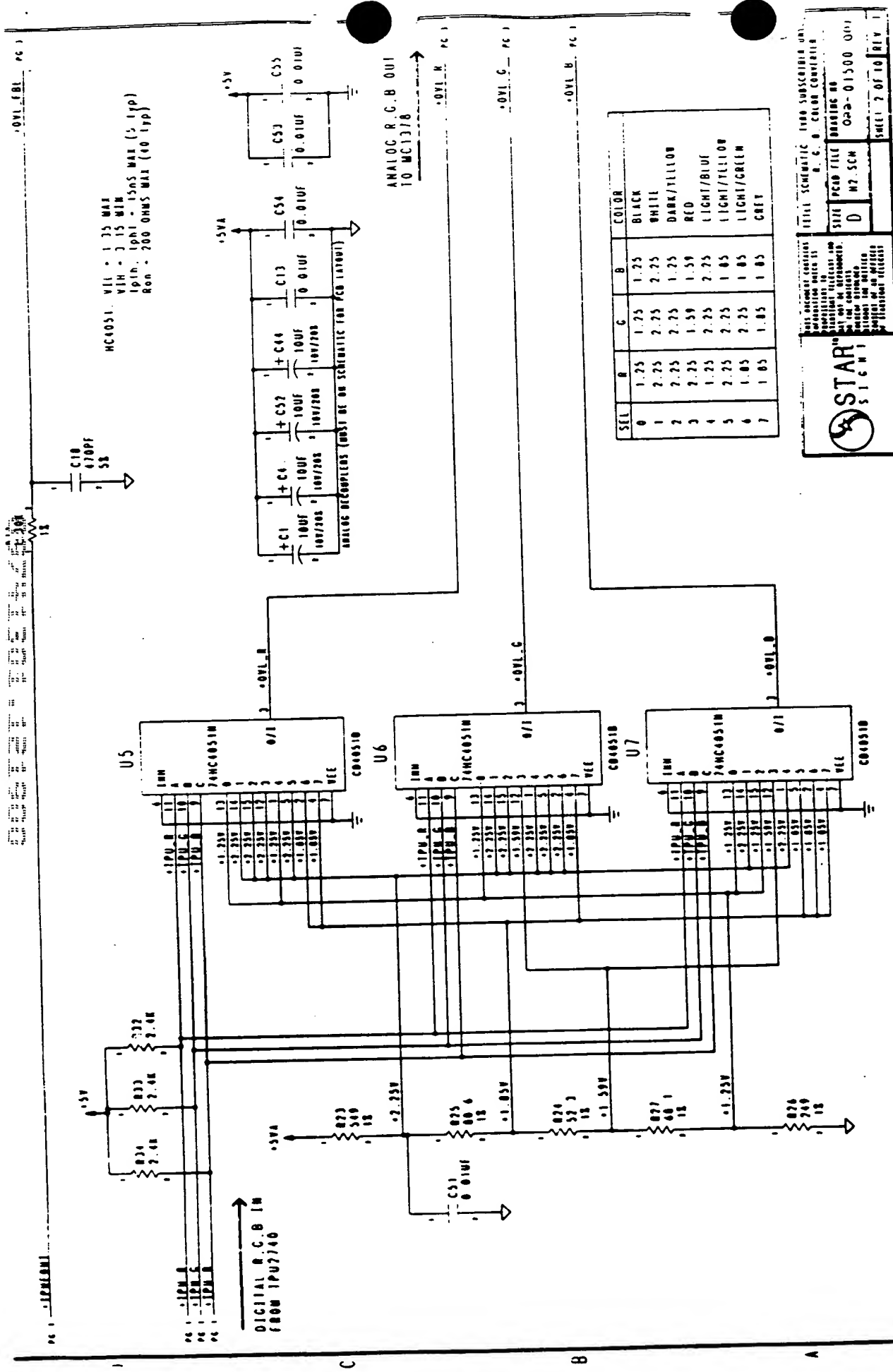
| COMPONENTS ADDED TO PCB WITH REVISION  | REFERENCE PART NO.   | UNUSED GATES   |
|--|--|--|
| U2B, R84, C101, D15, P7, J3, J1, L4, Q5, J5, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100 | U2B, R84, C101, D15, P7, J3, J1, L4, Q5, J5, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100 | U2B, R84, C101, D15, P7, J3, J1, L4, Q5, J5, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100 |



STAR  
S I C M I

COMPONENTS ADDED TO PCB WITH REVISION  
R03, J36, PG 10





STAR SIGN

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FILE SCHEMATIC TWO SUBSIDIARY ONE  
R. C. B. COLOR CONVERSION

SIZE D  
PCAD FILE D  
W2 SCH  
Dwg No 022-01500 Q101

SHEET 2 OF 10 REV 1

| SEL | R    | G    | B    | COLOR        |
|-----|------|------|------|--------------|
| 0   | 1.25 | 1.25 | 1.25 | BLACK        |
| 1   | 2.25 | 2.25 | 2.25 | WHITE        |
| 2   | 2.25 | 2.25 | 1.25 | DARK/YELLOW  |
| 3   | 2.25 | 1.59 | 1.59 | RED          |
| 4   | 1.25 | 2.25 | 2.25 | LIGHT/BLUE   |
| 5   | 2.25 | 2.25 | 1.85 | LIGHT/YELLOW |
| 6   | 1.85 | 2.25 | 1.85 | LIGHT/GREEN  |
| 7   | 1.85 | 1.85 | 1.85 | GREY         |

# (R. C. B. 10 VIDEO) AND (OVERLAY GENERATION)

(G-12)

BCA1

BASICAL PG 1

V. SYN PG 1

MOD. VID. PG 10

VIDEO OUT

(RCA YELLOW)

VIDEO OUT

VIDEO OUT

VIDEO OUT

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VIDEO OUT

HORIZ VCO FILTER

HORIZ VCO TAKE

COLOR BURST FILTER

COLOR BURST VCO LOCKS TO

0 TIMES THE COLOR BURST

(A 3.579545 - 14.31010 MHz)

ADJUST C39 UNTIL BURST GATE - 15750 MZ

HORIZ VCO TAKE

COLOR BURST FILTER

COLOR BURST VCO LOCKS TO

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(A 3.579545 - 14.31010 MHz)

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(A 3.579545 - 14.31010 MHz)

ADJUST C39 UNTIL BURST GATE - 15750 MZ

HORIZ VCO TAKE

COLOR BURST FILTER

HORIZ VCO LOCKS TO 25A LINES

HORIZONTAL SYNC BALT

(750 8 15 124 26 RMZ - 0.021971 RMZ)

ADJUST C39 UNTIL BURST GATE - 15750 MZ

HORIZ VCO TAKE

COLOR BURST FILTER

COLOR BURST VCO LOCKS TO

0 TIMES THE COLOR BURST

(A 3.579545 - 14.31010 MHz)

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(A 3.579545 - 14.31010 MHz)

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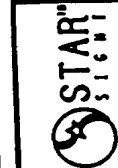
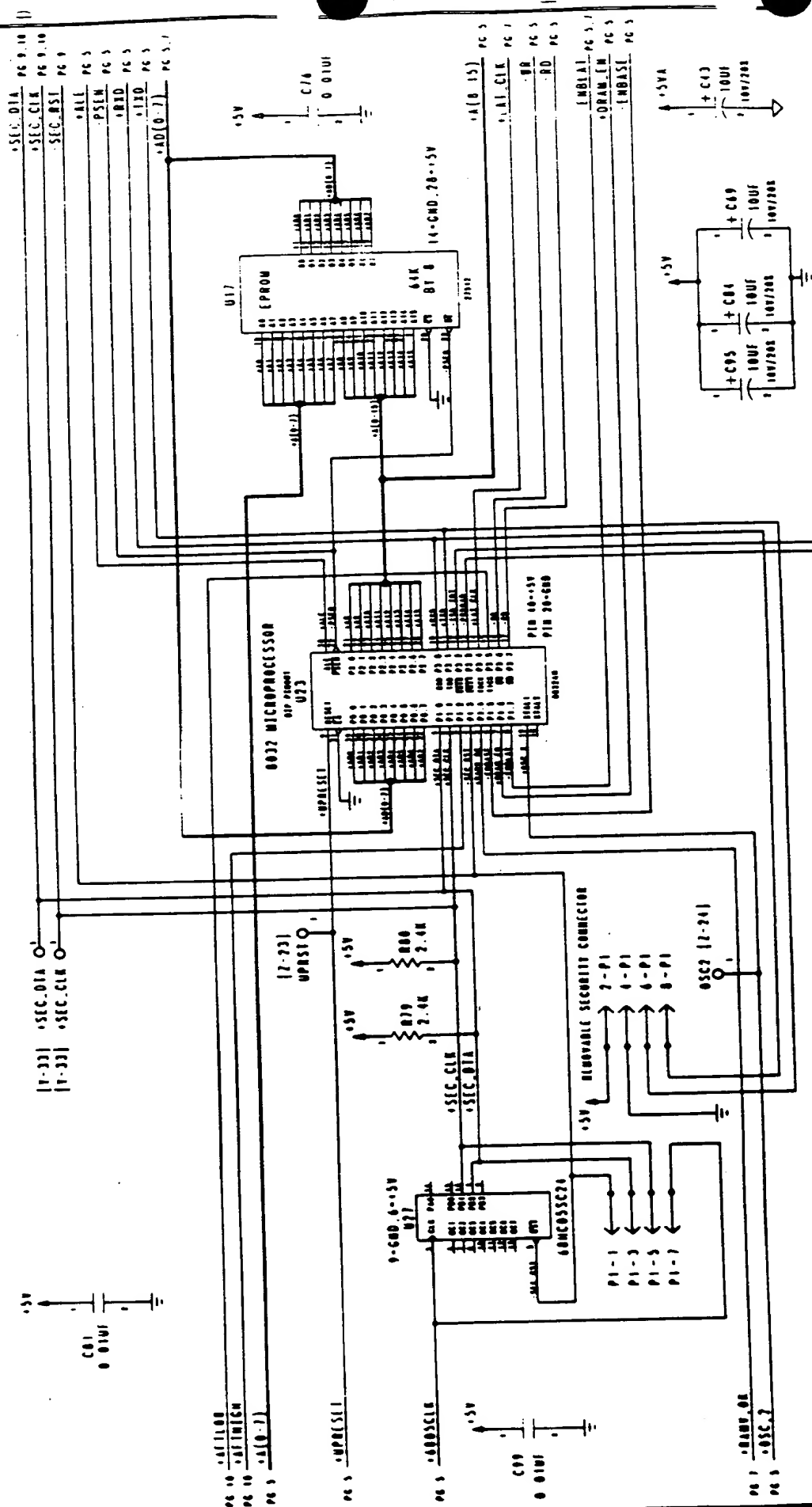
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PG 44

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DATE 08-01-2001 BY 60322  
UCBAW/BJA

**APPROVALS**

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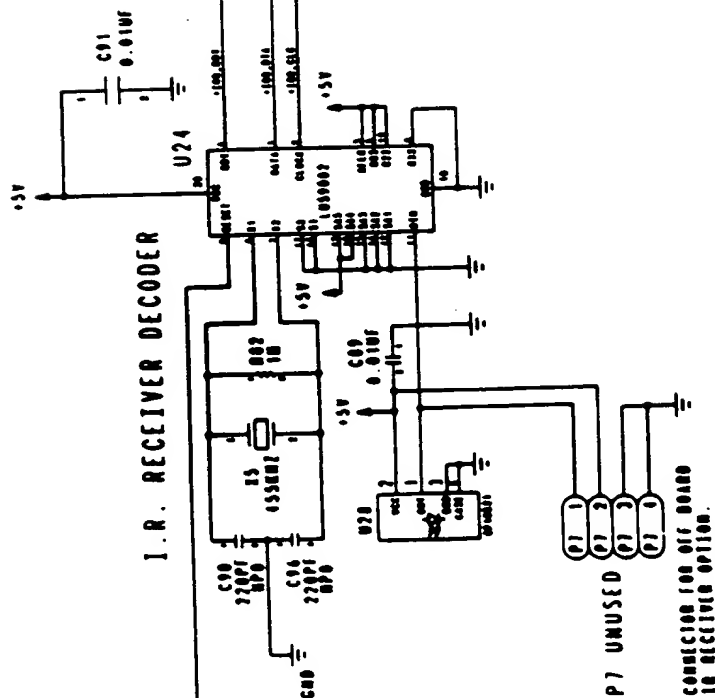
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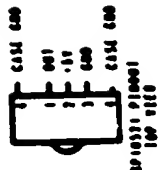
(10) - 1000 - (1)

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I.R. RECEIVER DIODE MODULE

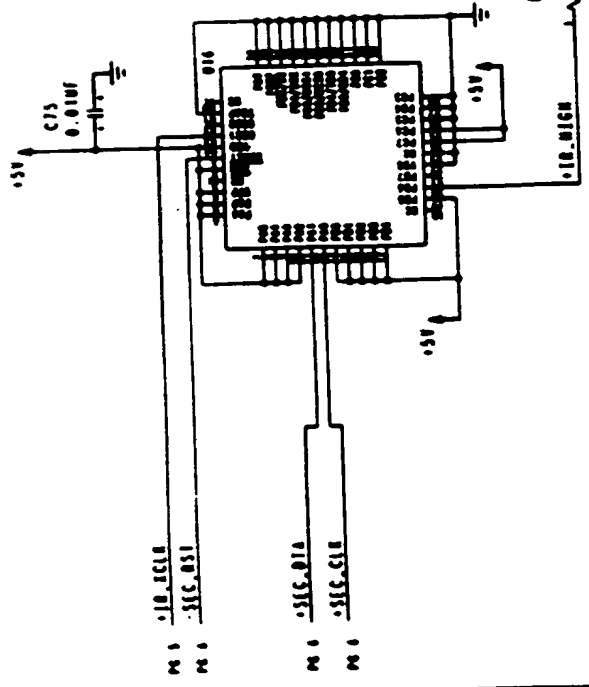


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S I E M E N T

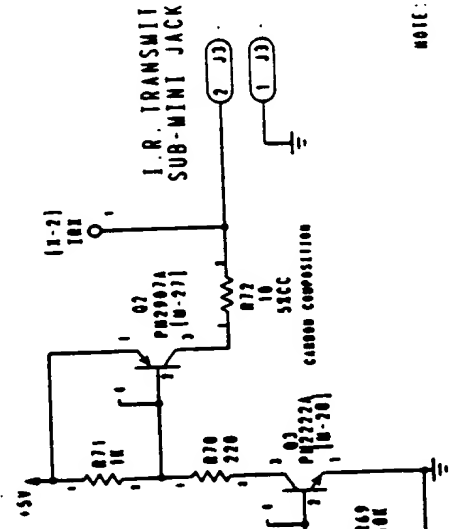
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INFORMATION CONTAINED  
HEREIN FOR THE  
PERFORMANCE OF THEIR  
DUTY.

|   |               |
|---|---------------|
| TITLE SCHEMATIC, 1980 SUBSCRIPTION UNIT |               |
| SIZE D                                  | NO. SCM       |
| SIZE 060 FILE                           | DRIVING NO    |
| 0                                       | 012-01500-001 |
| SHEET 0 OF 10 REV 1                     |               |

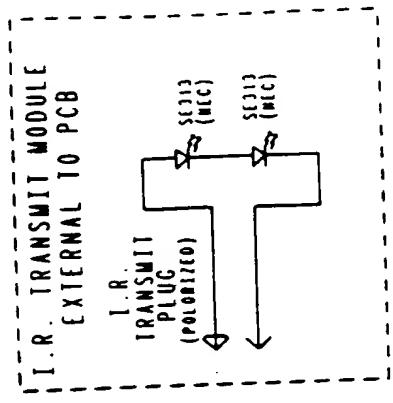
# I. R. TRANSMITTER



UNIVERSAL I. R. REMOTE  
TRANSMIT CHIP



I. R. DRIVER



NOTE: ALTERNATE I. R. DIODE - STANLEY AND 304

|                        |                           |   |
|------------------------|---------------------------|---|
| <b>STAR</b><br>SYSTEMS |                           | TITLE SCHEMATIC, I.R. TRANSMITTER UNIT<br>I. R. TRANSMITTER |
| SIZE<br>0              | PCB FILE<br>022-01500-001 | SHEET 9 OF 10 REV 1   |

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